5 March 2026

dorsaVi Establishes World-Class Technical Advisory Board as it Advances 'Intelligence at the Ultra Edge' Strategy

Strategic appointments of global leaders in RRAM, Neuromorphic Computing, and Robotics to guide commercialisation across Autonomous Systems, Robotics and Healthcare markets.

Key Highlights

- **Advisory Board established to advance "Intelligence at the Ultra Edge" strategy**, embedding sensing, memory and computation directly into robotics, wearables and autonomous systems.
- **Appointment of Mr Roger Peniche as Robotics Technical Advisor**, bringing over 25 years of global robotics and automation experience across Omron Robotics, Signode, Danaher, Fortive and General Electric.
- **Appointment of Professor Wen Siang Lew as Advanced Memory Strategic Adviser,** an internationally recognised expert in RRAM and nanoelectronic materials with 25+ years' experience at the University of Cambridge and Nanyang Technological University.
- **Appointment of Professor Shahar Kvatinsky as Neuromorphic and Processing-in-Memory Adviser**, Dean of the Viterbi Faculty of Electrical and Computer Engineering at Technion and a global leader in memristor/RRAM-based logic and Processing-in-Memory architectures.
- **Advisory Board underpins dorsaVi's "Intelligence at the Ultra Edge" vision**, integrating sensing, memory and decision-making in compact silicon to deliver microsecond-class response, nanojoule-class energy use and in-memory learning in robotics, wearables and autonomous systems.
- **RRAM platform positioned as the hardware foundation**, with 22 nm development focused on high-speed, low-voltage, non-volatile arrays that enable in-memory computing for both current clinical/workplace products and future robotics controllers.
- **Neuromorphic Reflex Engine and Adaptive Interface provides the intelligence layer**, using the RRAM fabric as artificial synapses to enhance gait and posture analytics today and enable real-time fusion, safety responses and on-device learning in future robotics and autonomous systems.
- **Unified technical chain from materials to deployment**, with the trio of Lew (devices), Kvatinsky (circuits/architectures) and Peniche (systems/markets) aligning dorsaVi's advanced memory, neuromorphic IP and RRAM-enabled robotic reflex modules with global opportunities, including EU AI Act–ready industrial and medical robotics.

Melbourne, Australia – 5 March 2026 – dorsaVi Limited (ASX: DVL) ("dorsaVi" or "the Company") is pleased to announce the establishment of a world-class Technical Advisory Board. This board will help guide the Company's transition from research-grade validation of its next generation RRAM-enabled neuromorphic sensor and robotics platforms into full scale system for commercial rollout.

The Advisory Board will support dorsaVi's **"Intelligence at the Ultra Edge" strategy** – devices in which sensing, memory and decision-making are tightly integrated in compact silicon, enabling robots, wearables and autonomous systems to interpret motion, learn from experience and act locally with minimal power, latency and cloud dependency delivering:

- **Microsecond-class response times** for safety-critical systems.
- **Nanojoule-class energy consumption**, drastically extending the battery life of wearable and portable devices.
- **In-memory computing**, allowing sensors to learn and adapt to specific environments locally

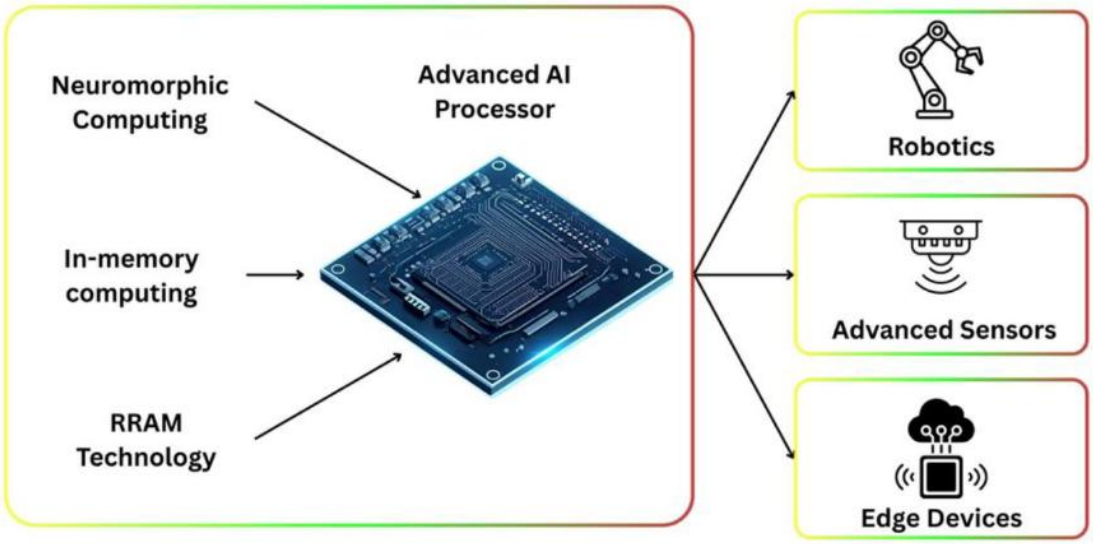


Figure 1: Conceptual illustration of how RRAM memory, in-memory computing and neuromorphic (brain-inspired) circuits are combined in a single advanced edge-AI processor. By storing data, learning patterns and running AI models directly on the chip, this architecture enables robots, sensors and other edge devices to respond faster, use less power and become progressively more intelligent over time.

Mathew Regan, Group Chief Executive Officer of dorsaVi, said: "With the appointments of Roger, Professor Lew and Professor Kvatinsky, we've assembled a complementary advisory trio that gives us world-class guidance across the entire technology stack – from materials and device physics, through neuromorphic and Processing-in-Memory architectures, to real-world robotics deployment. That's exactly the mix of experience we need to execute on our 'Intelligence at the Ultra Edge' strategy. It gives us confidence that we can take our RRAM and neuromorphic IP from the lab into robust, certifiable products for autonomous systems, robotics and healthcare markets, and do so in a way that is grounded in both scientific excellence and commercial scalability."

A Unified Technical Chain: From Advanced Materials to Global Robotics Deployment

Mr. Roger Peniche (Robotics Advisor)

Mr. Peniche brings more than **25 years of global experience** in robotics, mechatronics, and advanced manufacturing. He currently serves as the **Vice President of Engineering and Manufacturing at Omron Robotics**, where he leads multi-disciplinary teams delivering high-precision manipulators and autonomous mobile robots (AMRs) for industrial and medical environments.

- **Professional Pedigree:** Previously held senior leadership roles at **Signode** (VP of R&D), **Danaher/Fortive** (VP of Engineering and Quality), and **General Electric**.
- **Operational Excellence:** At Signode, he oversaw the delivery of new products that drove around 20% vitality growth at approximately 15% lower cost while quadrupling patent generation, rebooting innovation culture through IP incentives and structured hackathons.
- **Biomedical & Regulatory Expertise:** During a decade at Danaher, he implemented global collaboration frameworks that reduced costs while maintaining compliance in regulated environments and strengthened the reliability of **FDA-regulated systems**.
- **Strategic Role at dorsaVi:** He will guide the commercial and engineering development of RRAM-enabled robotics, transitioning dorsaVi's core sensors from passive data capture to active, reflexive control systems for AMRs, collaborative robots (cobots) and other safety-critical platforms.
- **European Market Opportunity:** He will play a critical role in preparing the Company's platforms for the European market, aligning technology with the stringent requirements of the EU AI Act and Machinery Regulation.

Mr Peniche holds an Engineer's Degree in Mechanical and Electrical Engineering and a Master's in Computational Fluid Dynamics and Structural Mechanics (Aeronautics), a postgrad in Software, and is an inventor on multiple industrial and mechatronics patents across transformer safety and connection hardware, automotive wheel-balancing components and packaging/ASRS automation mechanisms.

Collectively, this experience positions Mr Peniche as a key bridge between dorsaVi's advanced memory and neuromorphic technologies and the practical realities of deploying robots at scale in regulated industrial and healthcare settings. He brings a deep understanding of how controllers, sensors and safety systems must be architected for real-world environments, how to design for manufacturability and reliability from day one, and how to navigate the commercial and regulatory hurdles that sit between a promising technology and a certified product on a factory floor or in a hospital. At dorsaVi, he will help ensure that the Company's RRAM-enabled robotic reflex and sensor modules are specified against genuine customer requirements, are straightforward to integrate into existing control stacks, and evolve in a way that supports long-term partnerships with robotics OEMs and system integrators in Europe and globally.

Professor Shahar Kvatinsky (Neuromorphic and Processing-in-Memory Adviser)

Professor Kvatinsky is a world-leading authority on emerging non-volatile memories, memristor/RRAM-based logic, and Processing-in-Memory (PIM) architectures. He serves as the Dean of the Viterbi Faculty of Electrical and Computer Engineering at Technion – Israel Institute of Technology.

- **Academic Leadership**: Holds the Joan Goldberg Arbuse Chair in Electronics and leads the ASIC² research group and the Architectures and Circuits Research Centre (ACRC), both focused on next-generation circuits and architectures that tightly couple memory and compute.
- **Technical Portfolio:** A pioneer of memristor-based logic families such as MAGIC and IMPLY, and of compute-in-memory circuits that perform operations directly inside memory arrays to remove the latency and energy bottlenecks inherent to traditional CPU-to-memory architectures.
- **Industry & Research Background:** Previously a Visiting Professor at the University of Toronto and a Postdoctoral Fellow at Stanford University, with foundational experience in analog circuit design at Intel.
- **Strategic Role at dorsaVi:** Professor Kvatinsky will provide guidance on the architecture of the Company's neuromorphic Reflex Engine and PIM blocks, focusing on co-optimisation of RRAM devices, peripheral circuits and neuromorphic algorithms so the memory fabric is tuned for real-world sensing and edge-AI acceleration.

Taken together, this background places Professor Kvatinsky at the centre of dorsaVi's effort to turn its RRAM technology into a truly intelligent computing fabric at the ultra-edge. He brings a rare end-to-end perspective that spans device behaviour and array organisation through to full system architecture and has spent his career designing circuits that move computation into the memory itself – exactly the paradigm dorsaVi is pursuing with its Reflex Engine and Processing-in-Memory platform.

At dorsaVi, he will help translate laboratory-proven PIM and neuromorphic concepts into robust, synthesizable building blocks that can live inside real silicon, operate within tight power and area budgets, and interface cleanly with sensors, actuators and safety systems in robotics and healthcare environments. His guidance will be instrumental in ensuring that the Company's neuromorphic algorithms, learning rules and dataflows are co-designed with the underlying RRAM devices and peripheral circuits, so that the resulting edge-AI accelerators are not only efficient on paper but deliver reliable, certifiable performance in deployed robots, wearables and industrial sensing platforms.

Professor Wen Siang Lew (Advanced Memory Adviser)

Professor Lew is an internationally recognised authority in resistive random-access memory (RRAM), memristive devices, and nanoelectronic materials. He brings over 25 years of experience from world-renowned research institutions, including the University of Cambridge and Nanyang Technological University (NTU).

- **Research Impact:** He has authored more than 300 peer-reviewed publications with over 4,500 citations and approximately 60,000 research reads, reflecting sustained leadership in RRAM switching physics, device reliability and emerging non-volatile memory technologies.
- **Device & Materials Expertise:** His work is fundamental to understanding resistive switching mechanisms, thin-film materials, device variability, endurance and scaling behaviour that are critical to advanced-node and in-memory computing architectures.
- **Strategic Role at dorsaVi:** Professor Lew will support the advancement of the Company's 22 nm RRAM program, developed with tier-one semiconductor partners and built on the Company's acquired RRAM technology, helping guide its progression from research-grade validation into full-scale device, system and platform development.

- **System Alignment:** He will inform material and device choices to ensure they align with downstream system-level and integration requirements across robotics, medical devices and industrial sensing, and remain compatible with mainstream CMOS manufacturing flows.

Collectively, Professor Lew's experience ensures that dorsaVi's RRAM roadmap is grounded in the realities of advanced-node device physics and long-term reliability, rather than just headline performance metrics. He brings a deep understanding of how subtle choices in materials, stack design and processing conditions translate into variability, endurance and retention outcomes once devices are scaled and manufactured at volume.

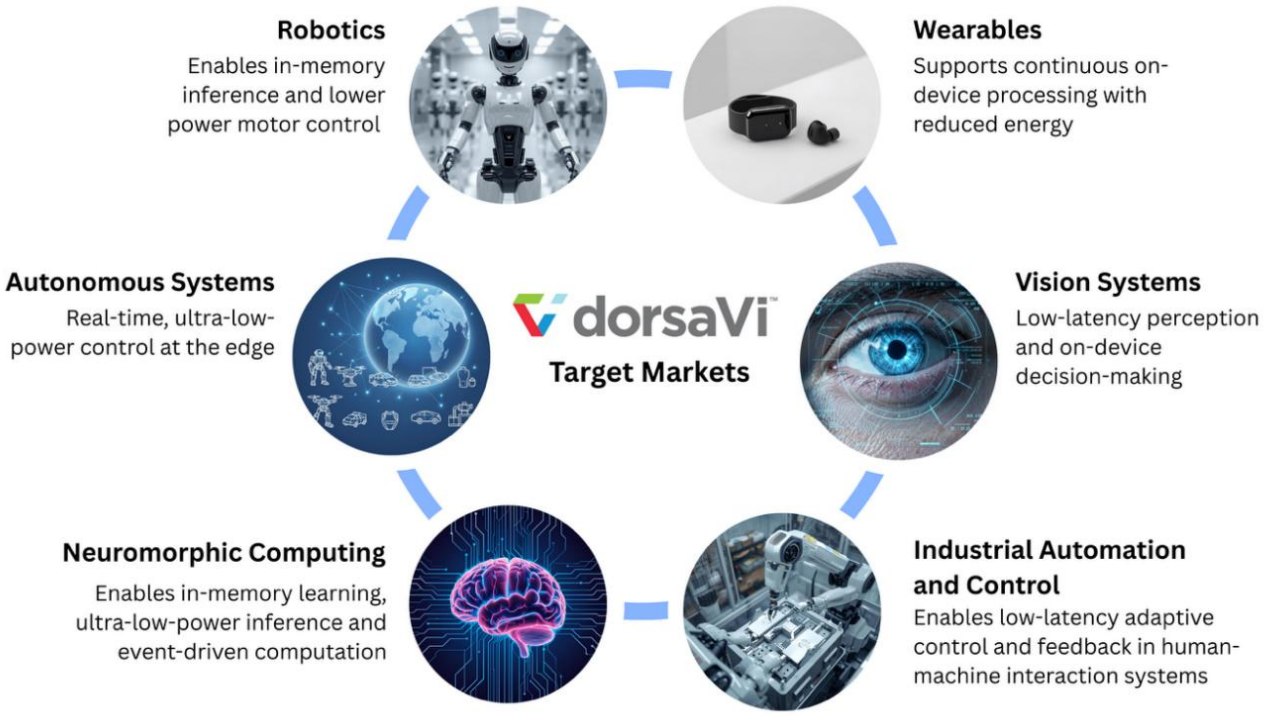
At dorsaVi, he will help shape the 22 nm RRAM platform so that it not only meets neuromorphic and in-memory computing needs in the lab, but can also be fabricated repeatably, characterised rigorously and trusted as the non-volatile backbone inside safety-critical systems. His guidance will be central to balancing innovation with robustness, ensuring that the Company's memory fabric is resilient under real-world operating conditions and can support the diverse requirements of robotics, medical devices and industrial sensing as "Intelligence at the Ultra Edge" is commercialised.

The "Ultra-Edge" Vision

Together, this trio enables dorsaVi to deliver **Intelligence at the Ultra Edge** – where sensing, memory and decision-making sit as close as possible to the point of use:

- **RRAM (Hardware – led by Prof. Lew):** Provides the high-speed, low-voltage, non-volatile memory foundation, giving the system a dense, efficient "storage and switching fabric" that can also support in-memory computation.
- **Neuromorphic IP (Intelligence – led by Prof. Kvatinsky):** Acts as the "brain", using the RRAM fabric as a grid of artificial synapses so the hardware can learn patterns, adapt over time and make decisions locally rather than relying on distant servers.
- **Deployment (Robotics & Systems – led by Mr Peniche):** Brings these capabilities into real products, resulting in controllers and sensor modules that respond within milliseconds, operate within tight energy and safety budgets, and can be certified and integrated into industrial, healthcare and autonomous systems.

Figure 2: Concept of DVLs Ultra Edge Vision



Intelligence at the Ultra Edge – Role of RRAM and Neuromorphic IP

Within dorsaVi's platform, RRAM provides the hardware foundation for "Intelligence at the Ultra Edge". The Company's RRAM technology is being developed to deliver high-speed, low-voltage switching with robust data retention, enabling dense, non-volatile memory arrays that can also support in-memory computing (where some computation is performed directly in the memory array rather than on a separate processor).

In dorsaVi's **existing clinical and workplace solutions**, these characteristics are expected over time to:

- allow more motion and muscle analysis to be performed directly on the wearable device;
- extend battery life by reducing memory and communication power; and
- support richer biofeedback and monitoring without continuous cloud connectivity.

Looking ahead to **robotics**, the same RRAM attributes – fast access, low energy use and in-array computation – are intended to underpin local reflex loops and control policies that run closer to the robot's sensors and actuators, helping controllers respond within milliseconds while operating within tight power and thermal budgets.

The Company's neuromorphic IP provides the intelligence layer that sits on top of this memory fabric. dorsaVi's neuromorphic portfolio, centred on the Reflex Engine and Adaptive Interface, is designed to execute inference and adaptation in or near memory (Processing-in-Memory), using the RRAM array as a dense grid of "artificial synapses".

In current **biosensing applications**, this capability is expected over time to:

- enhance anomaly detection in gait and posture;

- enable more personalised rehabilitation prompts; and
- deliver continuous monitoring based on local events rather than raw data streaming.

In future **robotics and autonomous systems**, the same neuromorphic structures can be used to:

- fuse data from multiple sensors (motion, force, muscle activity, environment);
- generate intent-aligned motion and safety responses in real time; and
- adapt to changing loads, surfaces and environments through on-device learning.

By pairing neuromorphic algorithms with RRAM as the underlying memory fabric, dorsaVi is targeting compact, energy-efficient "reflex modules" for robots and wearables – hardware blocks that sense, interpret and act at the ultra edge, rather than sending everything back to the cloud.

This release has been authorised for lodgement to the ASX by the Board.

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About dorsaVi

dorsaVi Ltd (ASX: DVL) is an ASX company focused on developing innovative motion analysis device technologies for use in clinical applications, elite sports, and occupational health and safety. dorsaVi believes its wearable sensor technology enables, for the first time, many aspects of detailed human movement and position to be accurately captured, quantified, and assessed outside a biomechanics lab, in both real-time and real situations for up to 24 hours. dorsaVi's focus is on two major markets:

- **Workplace:** dorsaVi enables employers to assess risk of injury for employees as well as test the effectiveness of proposed changes to OHS workplace design, equipment or methods based on objective evidence. dorsaVi works either directly with major corporations, or through an insurance company's customer base with the aim of reducing workplace compensation and claims. dorsaVi has been used by major corporations including London Underground, Vinci Construction, Crown Resorts, Caterpillar (US), Boeing, Monash Health, Coles, Woolworths, Toll, Toyota, Orora, Mineral Resources and BHP Billiton.

- **Clinical:** dorsaVi is transforming the management of patients with its clinical solutions (ViMove+) which provide objective assessment, monitoring outside the clinic and immediate biofeedback. The clinical market is broken down into physical therapy (physiotherapists),

hospital in the home and elite sports. Hospital in the home refers to the remote management of patients by clinicians outside of physical therapy (i.e. for orthopaedic conditions). Elite sports refer to the management and optimisation of athletes through objective evidence for decisions on return to play, measurement of biomechanics and immediate biofeedback to enable peak performance.

Further information is available at www.dorsaVi.com