dorsaVi™

SEMICONDUCTOR CONFERENCE PRESENTATION - MARCH 2026

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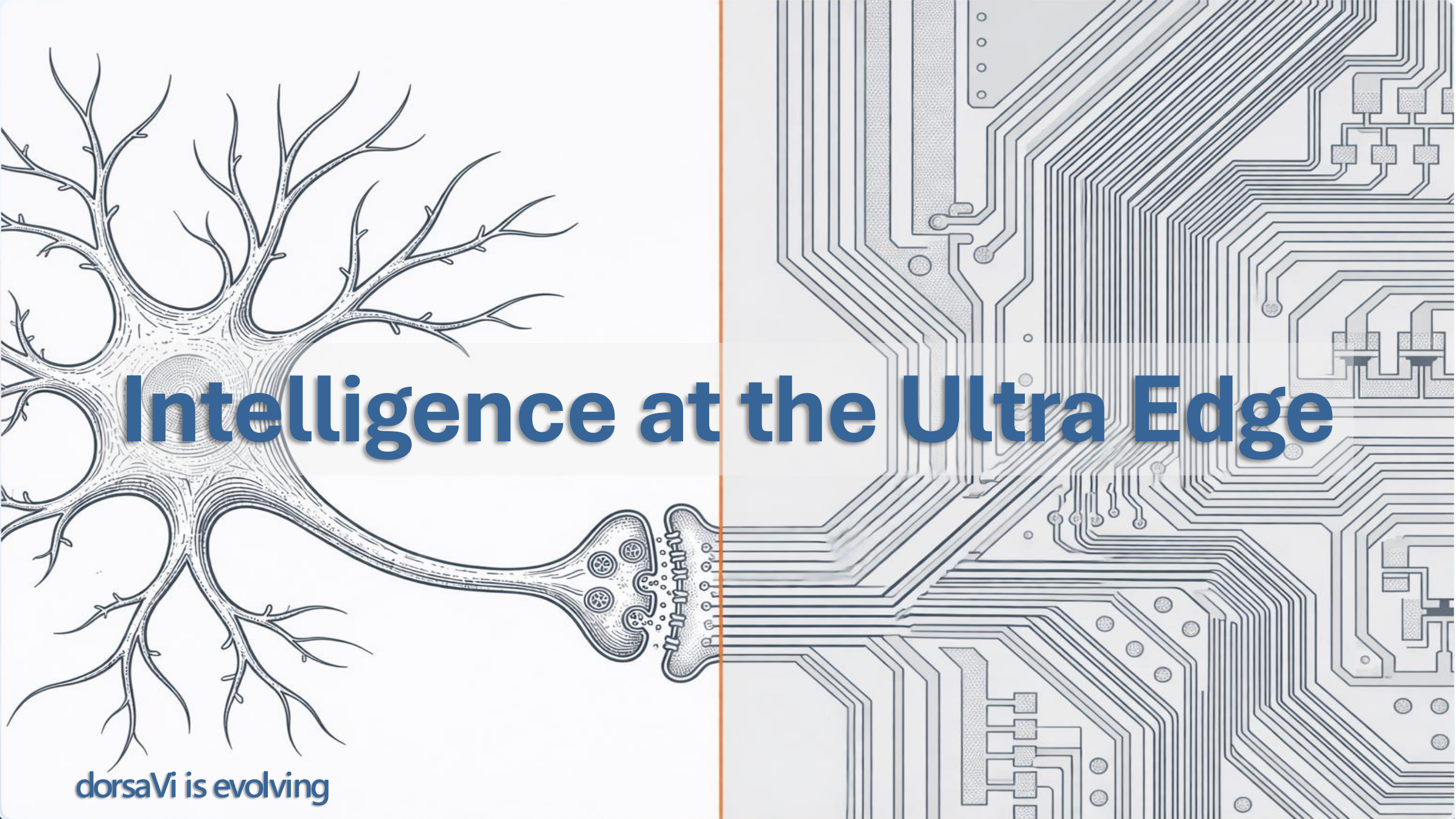


Intelligence at the Ultra Edge

dorsaVi is evolving

DorsaVi Today

Overview of dorsaVi's human movement and sensor business

ViMove+ ViSafe+ Research+

ViMove+ ViSafe+

- Fully integrated movement tracking sensors and software kit
- Delivers biomechanical data insights for clinics
- Video AI with real-time biomechanical analysis

FDA CLEARED

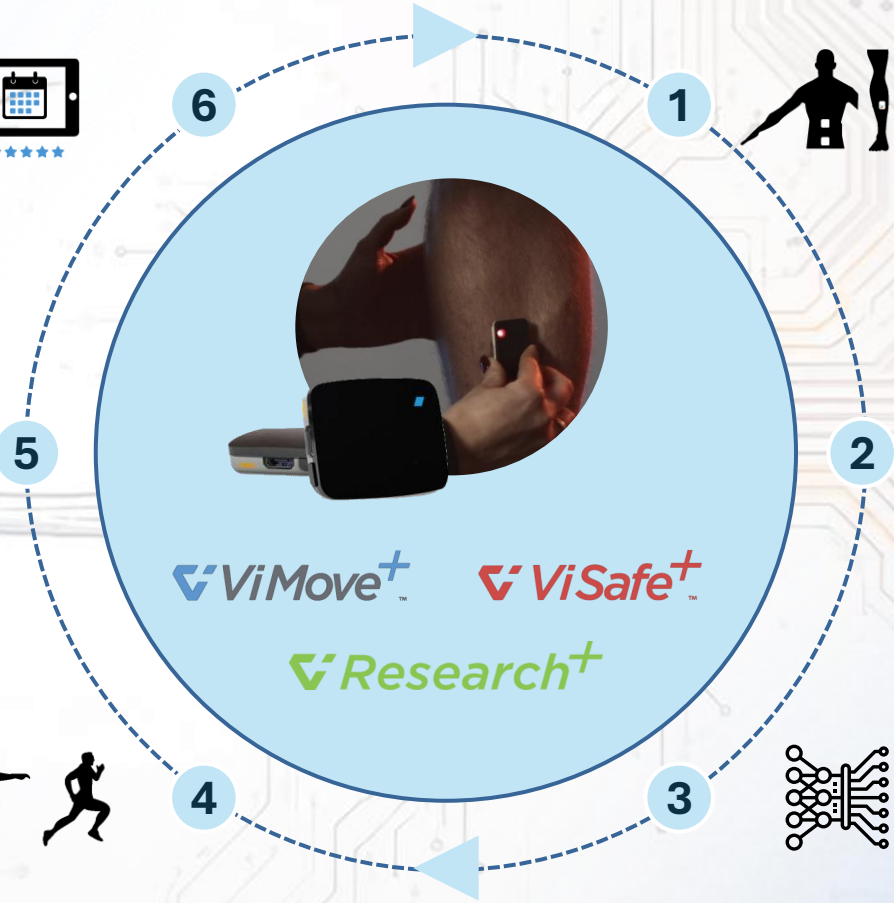
TGA CERTIFIED (Therapeutic Goods Administration of Australia)

- FDA-cleared and TGA-certified movement sensor platforms
- Clinically validated for real-world use outside the lab
- Deployed across clinical, workplace, and research settings
- Proven reliability under continuous-use conditions

Sensor Technology Overview

DorsaVi Captures data at the Ultra Edge and processes it either at the edge device or in the cloud

Medical-grade sensors and video tracking designed to analyse the way humans move



Insight That Changed the Strategy

Capturing data at the ultra-edge is not enough

Capturing data is not enough

- Real value is created when decisions are made and acted upon locally, without round-tripping to the cloud
- Latency, bandwidth and connectivity constraints make cloud-dependent inference a liability in critical environments
- High-quality human movement data is underutilised without local intelligence

Moving intelligence closer delivers:

- Lower latency
- Lower power consumption
- Higher reliability
- Scalable deployment into real environments

This drove investment into:

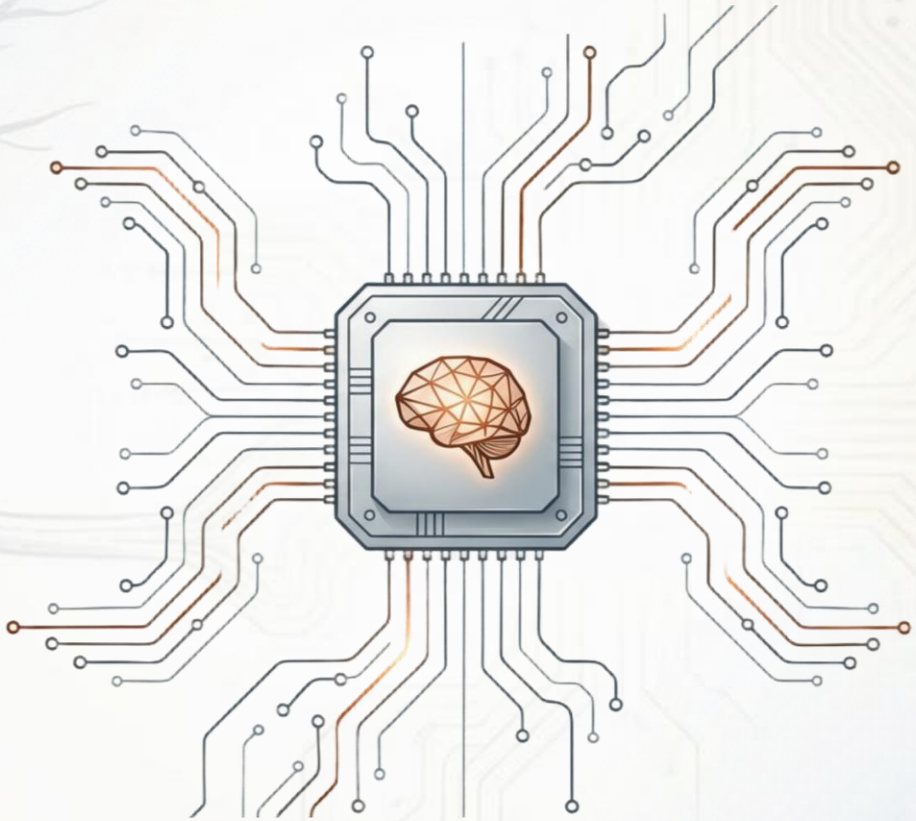
- Neuromorphic Computing (Technion) → Decision layer:
  reflex-based, event-driven intelligence mimicking neural efficiency
- RRAM Memory (NTU + ITRI) → Infrastructure layer:
  non-volatile, in-memory compute enabling persistent learning at the edge

World-Class Intellectual Property

Built on a foundation of exclusive licenses and technology partners

TECHNION
Israel Institute of Technology

License and Technology Parter:
Architecture and arrays emulating biological neurons



ITRI
Industrial Technology Research Institute

Manufacturing Partner:
Commercial IP readiness and manufacturing

NANYANG TECHNOLOGICAL UNIVERSITY SINGAPORE

License and Technology Partner:
Custom RRAM technology through physics and materials

Technical Advisory Board

Global Leaders in:
Neuromorphic Computing, RRAM and Robotics

An Overview of Our Partners

Capturing data at the ultra-edge is not enough

World-Class Partners:

ITRI
Industrial Technology Research Institute

6,500+
Researchers and Engineers

50+
Years advancing Taiwan's semiconductor ecosystem

TECHNION
Israel Institute of Technology

10,000+
Researchers affiliated with Technion

160+
Departments

NANYANG TECHNOLOGICAL UNIVERSITY
SINGAPORE

Top 5
Globally in multiple engineering fields*

10+ Years
Of RRAM and memristor device research

Robust IP Portfolio:

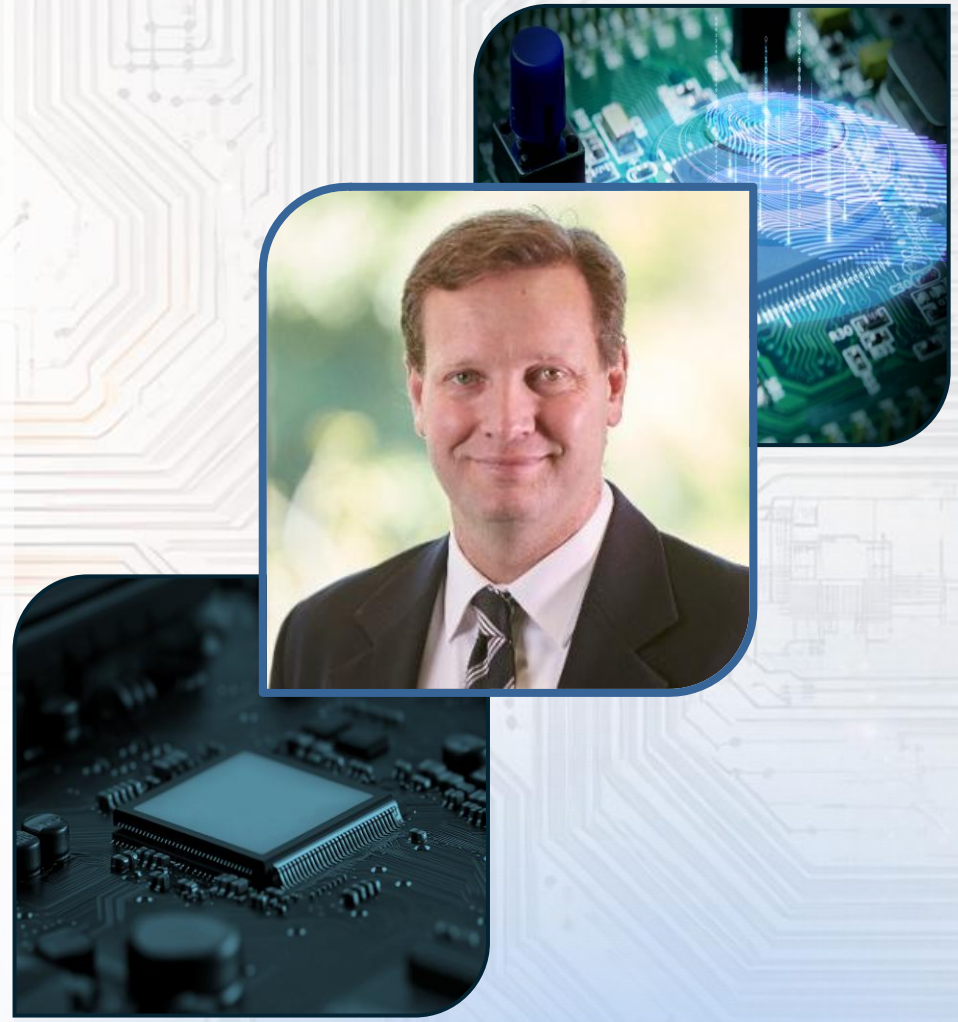
- Advanced semiconductor facilities for memory and compute development
- Fabrication lines and specialised equipment at advanced nodes
- Testing and validation capabilities for performance and reliability

*www.topuniversities.com/university-subject-rankings/electrical-electronic-engineering

Mathew Regan - Group CEO

Experienced Technology Operator

- Former Chief Executive Officer of Artrya Limited (ASX:AYA)
- Driving commercialisation across RRAM, robotics, neuromorphic computing and edge-AI platforms
- Led transformation, operations and growth across ASX-listed and technology-led businesses
- Strong strategy-to-execution leader with a proven ability to bridge vision, delivery and market growth
- Built a strong track record across strategy, execution and commercial growth
- Recognised for turning emerging technologies into scalable commercial platforms
- Held senior executive roles across Water Corporation, Quintis, IMDEX and CBH Group



Technical Advisory Board

Advisors: from advanced memory materials to neuromorphic computing and robotics deployment

Roger Peniche

Robotics & Advanced Manufacturing Advisor

- Former VP Engineering & Manufacturing – Omron Robotics
- 25+ years in robotics, mechatronics and automation systems
- Former leadership roles at General Electric, Fortive and Signode
- Advises on RRAM-enabled robotics, autonomous systems and EU regulatory alignment

Professor Shahar Kvatinsky

Neuromorphic Computing & Processing-in-Memory Advisor

- Dean, Electrical & Computer Engineering – Technion – Israel Institute of Technology
- Global leader in memristor/RRAM logic and Processing-in-Memory architectures
- Founder of ASIC^2 Research Group and ACRC
- Advises on neuromorphic Reflex Engine architecture and compute-in-memory integration

Professor Wen Siang Lew

Advanced Memory & RRAM Advisor

- Professor of Physics – Nanyang Technological University
- International expert in RRAM devices, nanoelectronics and switching physics
- 300+ publications in advanced memory technologies
- Guides development of dorsaVi's 22nm RRAM platform and device reliability

The Macro Tailwinds

Several structural macro tailwinds are reinforcing our strategy

Global memory bottlenecks are constraining AI scaling

- AI workloads are increasingly memory-bandwidth constrained rather than compute-limited
- Large models consume vast quantities of high-performance memory in data centres
- Moving intelligence toward the edge and ultra-edge reduces memory and data-movement pressure

AI data-centre growth is power-limiting

- Data-centre AI infrastructure is becoming power and cooling constrained
- Hyperscale AI deployments are approaching grid-scale power demand
- Edge processing reduces latency, power consumption, and network load

Explosion in edge devices, robotics, and autonomous systems

- Rapid growth in robotics, drones, autonomous systems and industrial IoT devices
- These systems require real-time local decision-making
- Ultra-low-power compute is needed for always-on sensing

Increasing geopolitical uncertainty is driving demand for local intelligence

- Defence, infrastructure, and industrial systems require local decision capability
- Data sovereignty and security concerns favour on-device AI

Intelligence at the Ultra-Edge

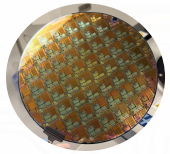
Enabling Devices to sense, decide and act locally

At the Sensor:

Sense → Decide → Act

Movement Sensors:

- World-leading, FDA-cleared sensor platform
- Currently captures movement data at the ultra-edge



Semiconductor RRAM Tech:

- Provides the infrastructure layer enabling high-speed, low-power neuromorphic computing

Neuromorphic Computing

- Brain-inspired architecture enabling real-time decision-making at the ultra-edge

Ultra-edge Semiconductor Computing

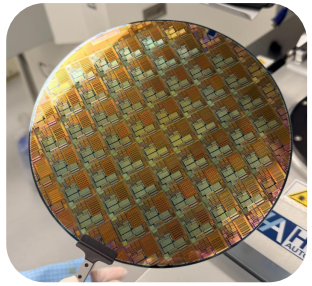
Overview of dorsaVi's ultra-edge enabling technology

Neuromorphic

Advanced brain-like chip architecture

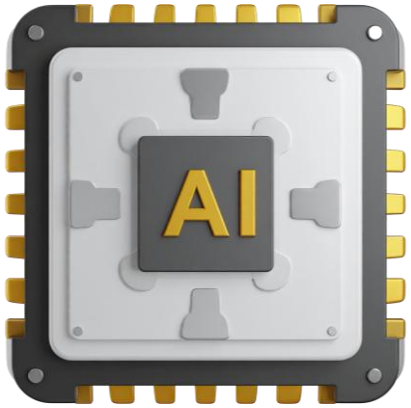
RRAM

Advanced 22nm technology node semiconductor technology



In-memory Computing

Reducing latency and increasing performance



Ultra-edge Applications

Key Applications our computing platform will impact

Wearables

Robotics

Artificial Intelligence

Industrial Automation

Autonomous Systems

Vision Systems

What is Neuromorphic Computing?

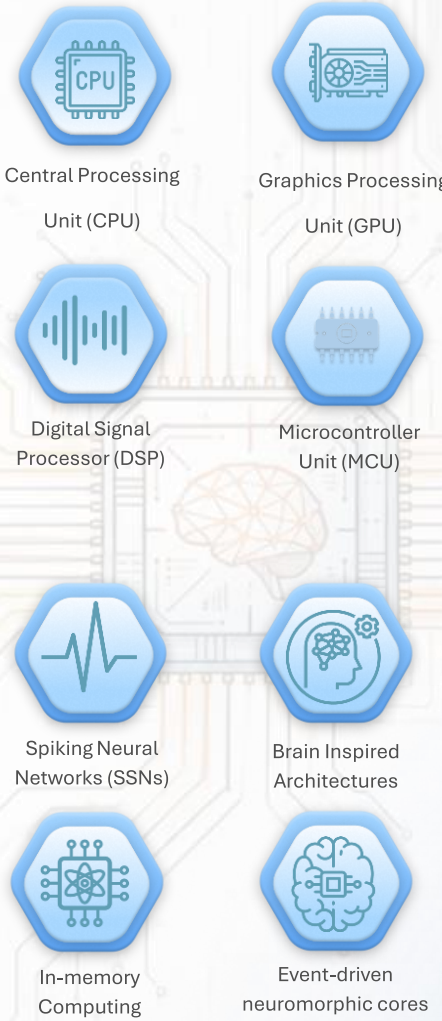
Types of Computing

Traditional Computing

- Binary arithmetic and logic operations
- Neural networks are simulated numerically, not native
- Continuous data shuttling between memory and compute

Neuromorphic Computing

- Information encoded in timing and events, not continuous values
- Neural behavior is implemented directly in hardware, not simulated
- Asynchronous, event-driven operation (no global clock)



Why Neuromorphic Computing

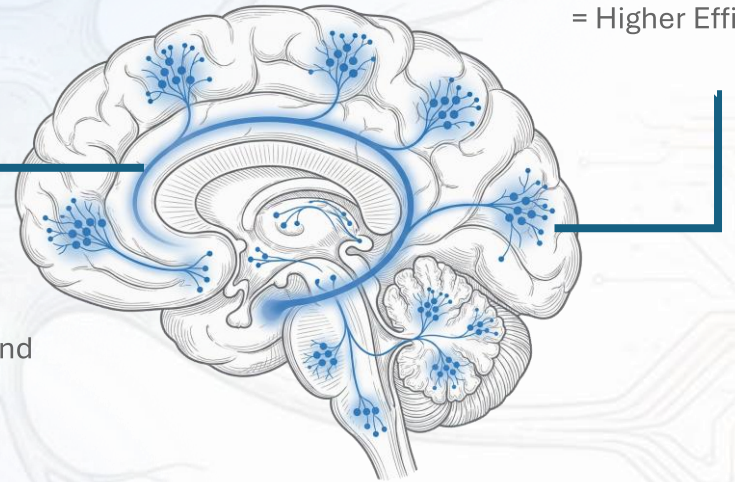
The “Energy Wall” at the Edge

Current chips waste ~90% of their energy moving data

The Biological Model

Neurons Compute and Store Memory

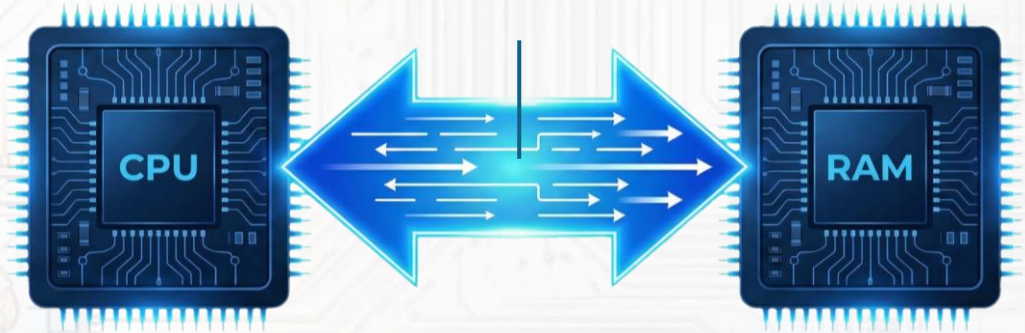
Local Processing = Higher Efficiency



Integrated Compute and Memory

The Von Neumann Bottleneck

High Latency & Power Drain



Compute Unit **Memory Store**

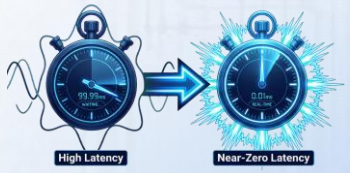
The Von Neumann Bottleneck

High latency and energy loss due to data travel. Separation of compute and memory needs continuous, power-hungry data transfer.

Power Constrained

Target markets operate on Coin-cell batteries (<1mW). Extremely limited power budget demands ultra-low-power solutions.

Speed Bottleneck



ANC and Drones require sub-millisecond response. Real-time applications demand instantaneous data processing without delays.

Key Features of our Neuromorphic Computing

Event-Driven Performance

- Event-triggered execution rather than continuous clock-driven processing
- Millisecond-class response enabled by local, in-memory inference
- Sub-microsecond inference paths at the processing-in-memory (PIM) level

Ultra-Low Power Operation

- Nanojoule-class inference enabled by in-memory processing
- Eliminates CPU–memory data shuttling and refresh overhead
- Designed to operate orders-of-magnitude closer to biological efficiency

Real-time Processing

- In-memory inference enables deterministic, closed-loop control
- Executes Sense → Decide → Act loops without cloud dependence
- Designed for real-time biosensing, robotics, and human–machine systems

About Neuromorphic Computing

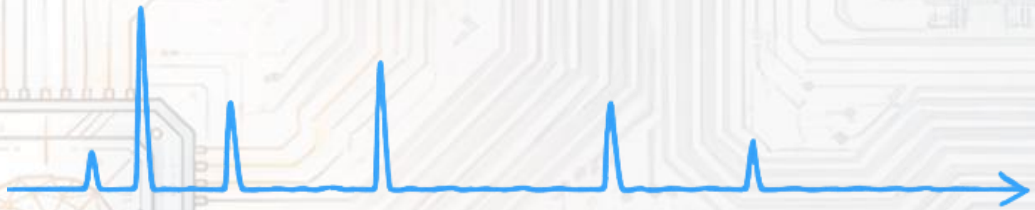
Event-Driven (SNN) Architecture for Ultra-Low-Power Edge AI

Standard Neural Network

Continuous Energy Consumption

- Continuous clock-driven execution
- Constant power draw - even when idle
- Processes all signals, regardless of relevance
- Optimized for dense, high-throughput data environments

Spiking Neural Network



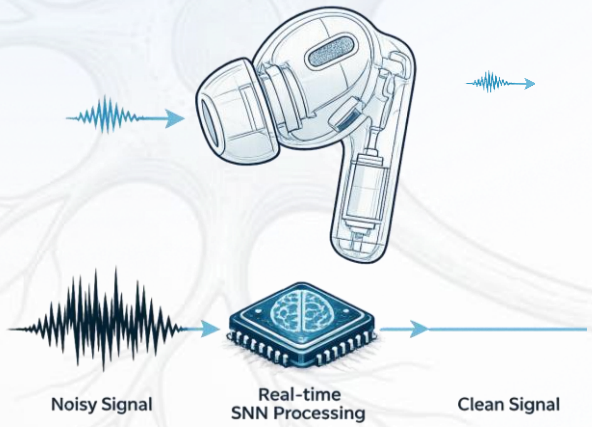
Zero Consumption = Silence

- Event-driven execution instead of continuous clock cycles
- Near-zero idle power consumption
- Activates only when meaningful signals occur
- Optimized for sparse, real-time edge data

Neuromorphic Use Cases

Current chips waste ~90% of their energy moving data

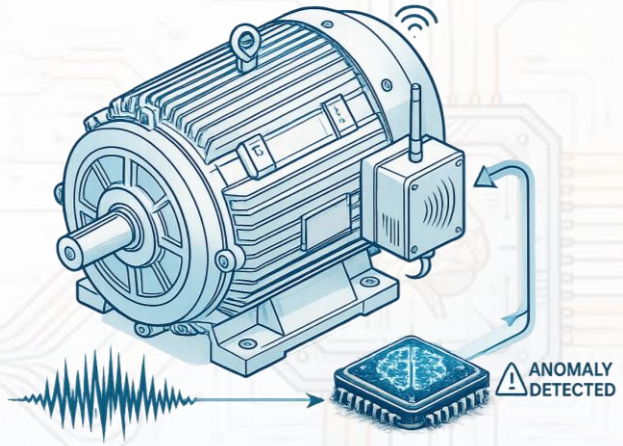
Adaptive Noise Cancellation (Hearables)



Problem:
Continuous Digital Signal Proce exceeds <1mW power budgets.

Solution:
Unsupervised on-chip learning detects anomalies locally.

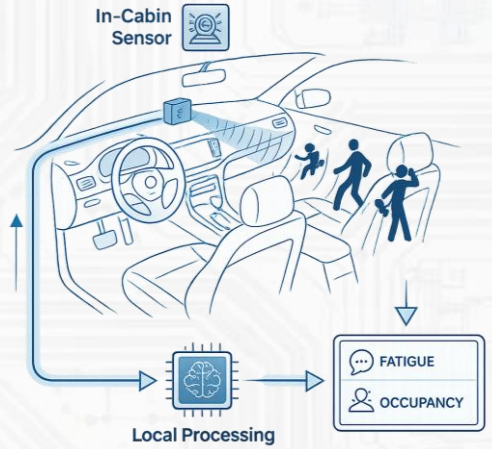
Industrial Predictive Maintenance



Problem:
Cloud-dependent training introduces latency and cost.

Solution:
Unsupervised on-chip learning detects anomalies locally.

Automotive In-cabin Monitoring



Problem:
Camera-based systems increase privacy exposure.

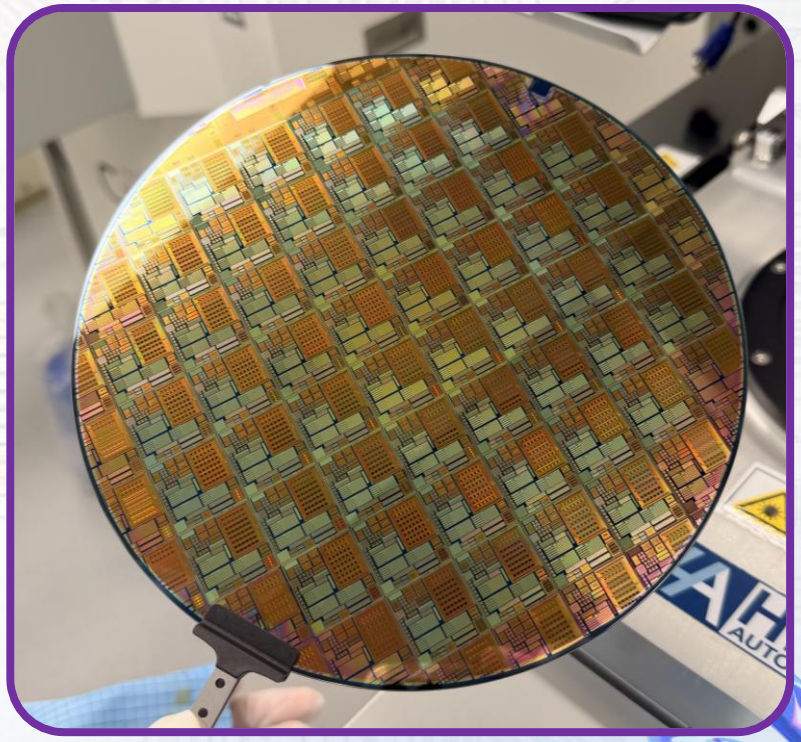
Solution:
Local radar inference detects fatigue and occupancy without video.

dorsaVi and NTU - RRAM

dorsaVi is moving to a market leading 22nm technology node size

Superior Performance

- >2× density scaling from 40 nm → 22 nm
  - Enables higher density compute-in-memory arrays
  - Reduces energy per inference
- ~20–30% lower write voltage, reducing energy per operation
- Faster access and switching enabled by advanced-node integration



Enables In-memory Computing

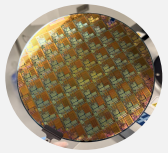
- >10 million cycles maintained at advanced node
- ~40°C improvement in retention robustness (>10 years from 85°C → 125°C)
- Suitable for continuous, write-intensive edge workloads

Binary and multi-state compute-in-memory

- Specialised in-memory compute architecture delivering >20 TOPS/W-class efficiency
- Order-of-magnitude gains in effective compute efficiency vs. memory-external architectures
- Brings computation into memory, reducing data flow

Tying Everything Together

How we Enable Intelligence at the Ultra-Edge

	Catalyst Layer	Infrastructure Layer	Intelligence Layer
Business Division	 Sensors	 RRAM	 Neuromorphic Computing
Key Focus	• Medical-grade wearable sensors capture real-world movement at the point of origin • FDA and TGA cleared sensors validate performance in regulated environments • Generates high-fidelity signals that initiate the intelligence loop	• Provides non-volatile, low-power memory tightly coupled to compute • Enables Compute-in-Memory (CIM), reducing data movement and energy consumption • Forms the hardware foundation for scalable Ultra Edge systems	• Event-driven processing inspired by biological systems. • Processes sensor data in real time with ultra-low latency and power • Executes the Decide function in the Sense → Decide → Act loop

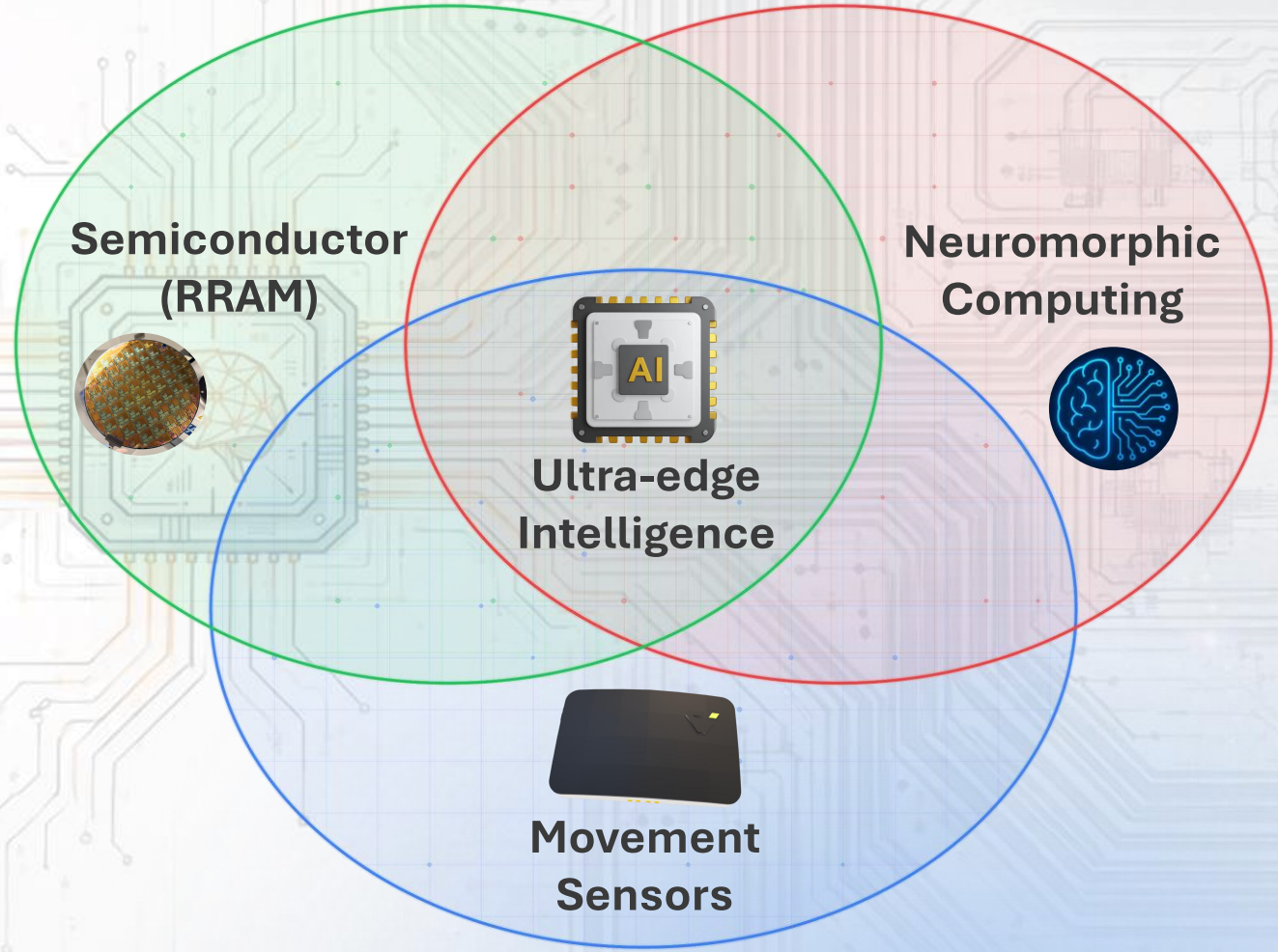
Tying Everything Together

Technology Convergence

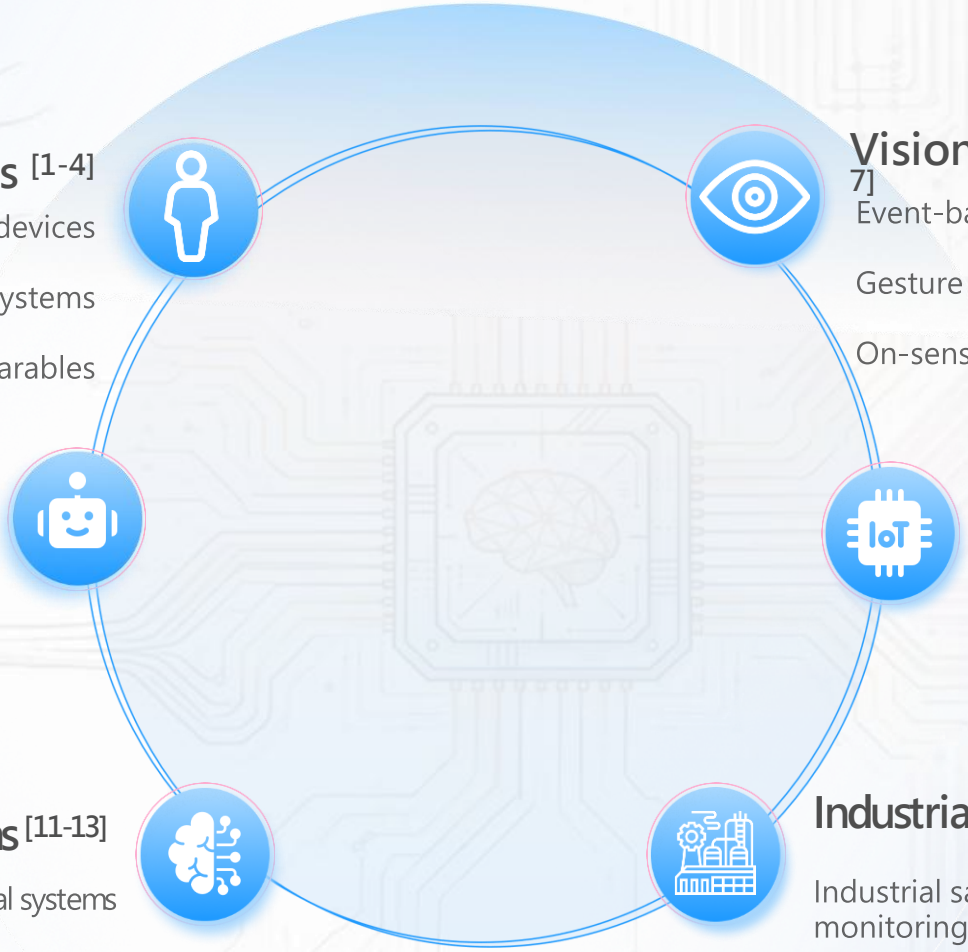
- **Sensors + RRAM**
  Improves the intelligence and efficiency of our existing sensor platforms

- **RRAM + Neuromorphic**
  Enables new ultra-edge AI hardware markets beyond traditional sensing

- **Sensors + Neuromorphic**
  Creates human-centric intelligent systems capable of real-time interpretation



Ultra-Edge Target Markets



Human-Centric Systems [1-4]

Medical devices

Assistive and prosthetic systems

Wearables

Set to hit **$723B** by 2030 — Human-Centric Systems

Vision & Perception Systems [5-7]

Event-based and edge vision

Gesture and motion recognition

On-sensor or near-sensor processing

Set to hit **$162B** by 2030 — Vision & Perception

Robotics [8-10]

Collaborative, mobile, and assistive robots

Human-safe, low-latency control

Local perception and decision-making

Set to hit **$218B** by 2031 — Robotics

IoT Devices [14-15]

Always-on edge sensing

Local AI inference

Ultra-low-power operation

Set to hit **$181B** by 2030 — IoT Devices

Autonomous Systems [11-13]

Drones and aerial systems

Autonomous vehicles and industrial autonomy

latency- and power-critical environments

Set to hit **$277B** by 2030 — Autonomous Systems

Industrial & Infrastructure [16]

Industrial safety and ergonomics monitoring

Edge monitoring and control

Reliability in remote or constrained settings

Set to hit **$45B** by 2030 — Industrial & Infrastructure

[1] Grand View Research, "Medical Devices Market Size, Share & Trends Analysis Report."|[2] Grand View Research, "Wearable Medical Devices Market Size, Share & Trends Analysis Report." | [3] Grand View Research, "Audiology Devices Market Size, Share & Trends Analysis Report." | [4] Grand View Research, "Prosthetics And Orthotics Market Size, Share & Trends Analysis Report." | [5] Precedence Research, "Machine Vision Market Size, Share & Trends Analysis Report." | [6] Grand View Research, "Machine Vision Market Size, Share & Trends Analysis Report." | [7] Grand View Research, "Gesture Recognition And Motion Sensors Market Size, Share & Trends Analysis Report." | [8] Mordor Intelligence, "Robotics Market Size & Share Analysis - Industry Research Report." | [9] Grand View Research, "Service Robotics Market Size, Share & Trends Analysis Report." | [10] Grand View Research, "Collaborative Robots Market Size, Share & Trends Analysis Report." | [11] Grand View Research, "Autonomous Vehicles Market Size, Share & Trends Analysis Report." | [12] Grand View Research, "Commercial Drone Market Size, Share & Trends Analysis Report." | [13] Grand View Research, "Unmanned Underwater Vehicles Market Size, Share & Trends Analysis Report." | [14] Grand View Research, "IoT Devices Market Size & Share Report, 2030." | [15] MarketsandMarkets, "Edge AI Hardware Market." | [16] Grand View Research, "Industrial Edge Market Size & Share Report, 2030."

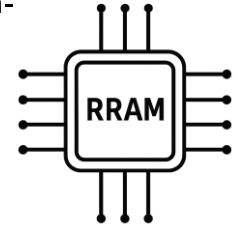
Our Competitive Advantage

Dorsavi controls key layers

- Memory device, array, and compute primitives
- Neuromorphic architecture and system design
- Path to software / tooling layer
- Implement in our own hardware first
- World Class Technical Advisory Board
- Human Movement Data Sets

Enabling the ultra edge

- Persistent on-device models (non-volatile)
- Minimal data movement, fast wake/sleep
- Always-on, ultra-low-power sensing
- Local decision-making, high privacy



Strong IP Moat

- Device physics + RRAM cell design
- Compute-in-memory arrays and circuits
- Neuromorphic execution + learning rules
- Compiler / toolchain leverage

Building towards in-memory-compute

- Binary and multi-state compute-in-memory
- Spiking neural network execution
- Sub-millisecond, reflex-grade decisions

Our Three Strategies

Key developments in parallel

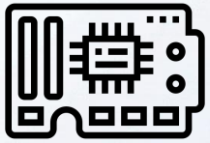
1

Evolve The Existing dorsaVi Platform

Move our FDA approved sensing closer to the ultra-edge

2

Develop 22nm RRAM with NTU and ITRI



Create the memory layer required for in-memory compute.

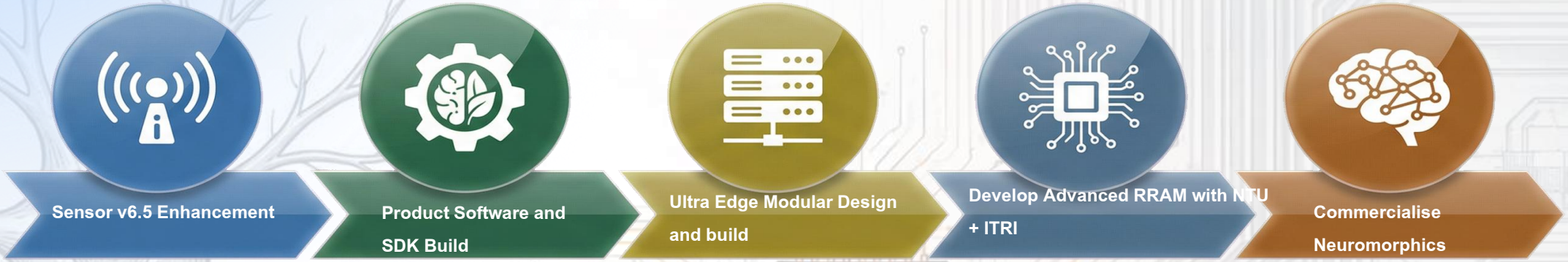
3

Unlock Neuromorphic Opportunities

Apply closed-loop intelligence - starting with ourselves.

Execution Plan

dorsaVi captures data at the ultra edge and processes it either at the edge device or in the cloud



- Integrate ultra-low power edge processing into Sensor v6.5
- Embed neuromorphic IP primitives
- Improve DSP for adaptive signal interpretation
- Validate closed-loop performance internally

- Upgrade iPad / Android clinical software
- Introduce real-time analytics and feedback
- Begin SDK framework for external partners

- Define modular device pack architecture
- Separate sensing, compute and memory layers
- Design power architecture for in-memory compute
- Prepare API layer for Ultra-Edge device integration

- Demonstrate proof-of-performance at 180nm
- Benchmark against 40nm lab data
- Validate scalability pathway to 22nm
- Define manufacturable 22nm RRAM process

- Deploy-reflex-based intelligence into our own platform
- Validate adaptive decision capability in live environments
- Identify priority commercial verticals
- Develop partnership pathways

Board and Management

Management

Mathew Regan
Group CEO

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Thank You

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