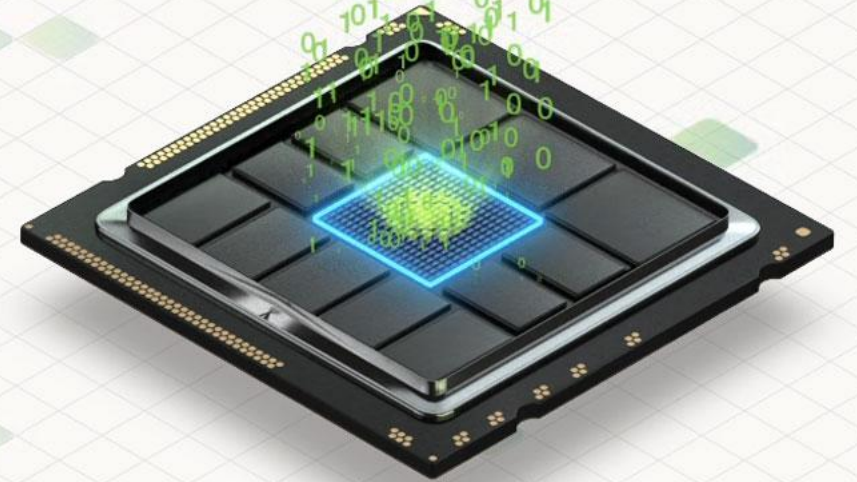




Investor Update

August 2026





Introduction



| Weebit Overview: Leading Vendor of ReRAM IP

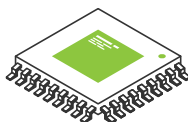
Advanced Non-Volatile Memory (NVM) Now Entering Production



Founded: 2015

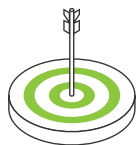
Located: Israel, France & US

Only independent provider of qualified ReRAM IP



Proven, protected technology

Fully qualified per JEDEC & AEC-Q100 standards; Available for chip designers
>100 patents/applications



Market opportunity

Meeting growing demand for embedded NVM and AI computing



Multiple commercial deals

Including **tier-1 IDMs TI & onsemi**;
Expanded deals with existing customers; Engagements with >20 foundries, IDMs, product companies



Profitable business model

Scalable, high-margin **IP licensing** to semiconductor companies & fabs



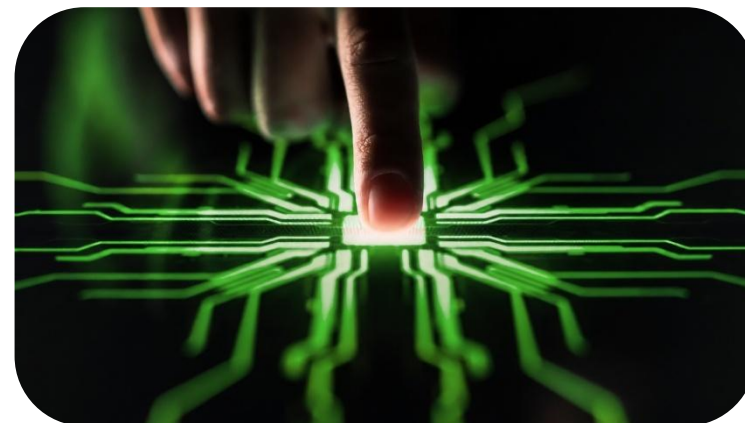
Financial strength / execution

Strong balance sheet supporting long-term growth; FY26 revenue expectation 3x FY25



Deep expertise

- Experts in Device, Process, Analog Design, and Digital Design & Algorithms
- >70 employees (**~90% engineers/ scientists**; ~20 PhDs)
- R&D partner: **CEA-Leti**, a leading microelectronics research institute



ASX: WBT

S&P/ASX 300

MSCI 
Global Small Cap Index

Strong and Experienced Board and Management Team

Coby Hanoch

CEO



David (Dadi) Perlmutter
CHAIRMAN



Anne Templeman-Jones
DEPUTY CHAIR



Atiq Raza
NON-EXEC.
DIRECTOR



Yoav Nissan-Cohen
NON-EXEC.
DIRECTOR



Naomi Simson
NON-EXEC.
DIRECTOR



Ashley Krongold
NON-EXEC.
DIRECTOR



Alla Felder
CFO



Ishai Naveh
CTO



Ilan Sever
CHIEF MEMORY
ARCHITECT



Lilach Zinger
VP
OPERATIONS



Issachar Ohana
CRO



Eran Briman
VP MARKETING &
BUSINESS DEV.



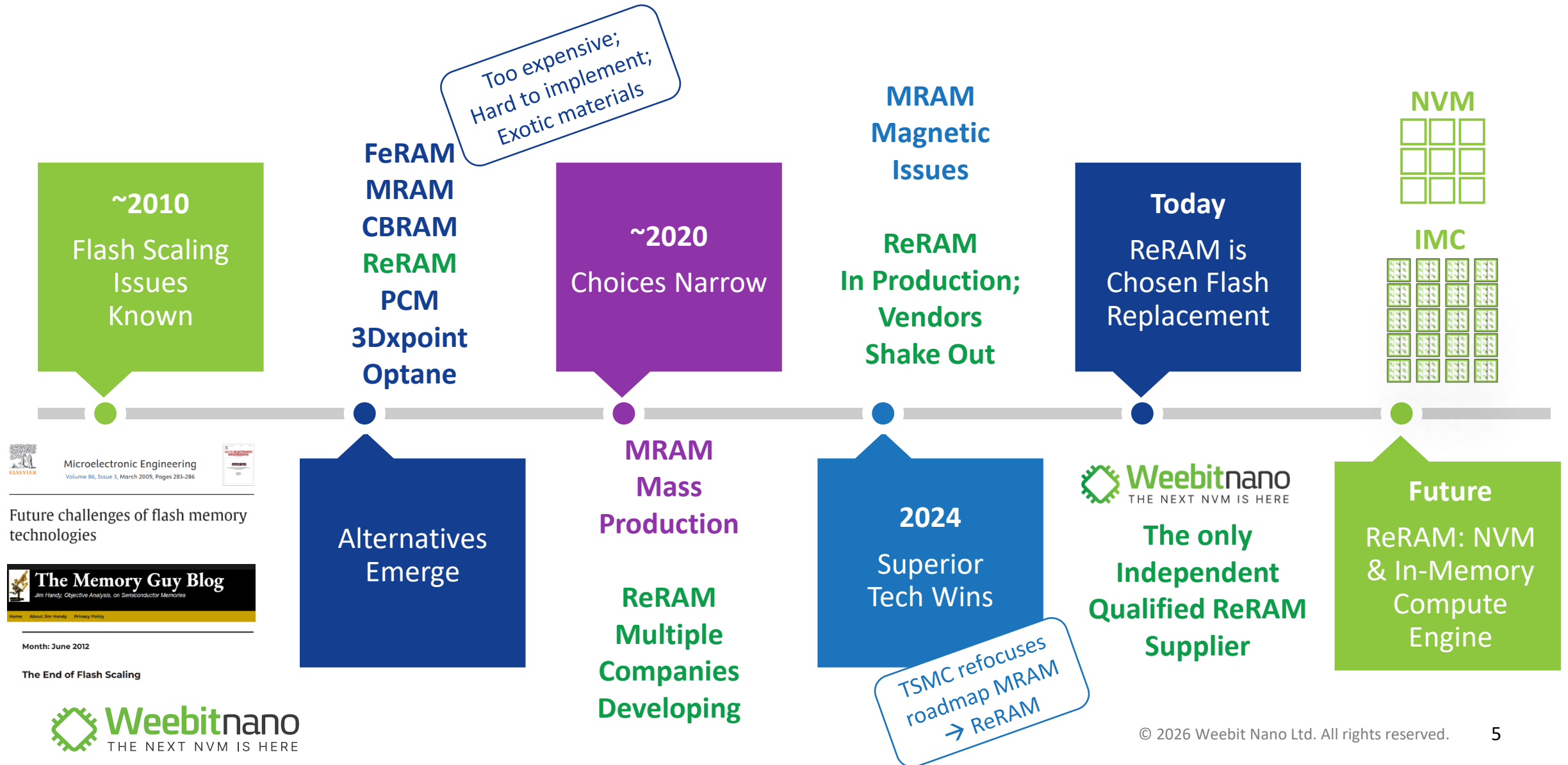
Gideon Intrater
VP SYSTEMS & AI



Raz Reshef
VP DESIGN
R&D



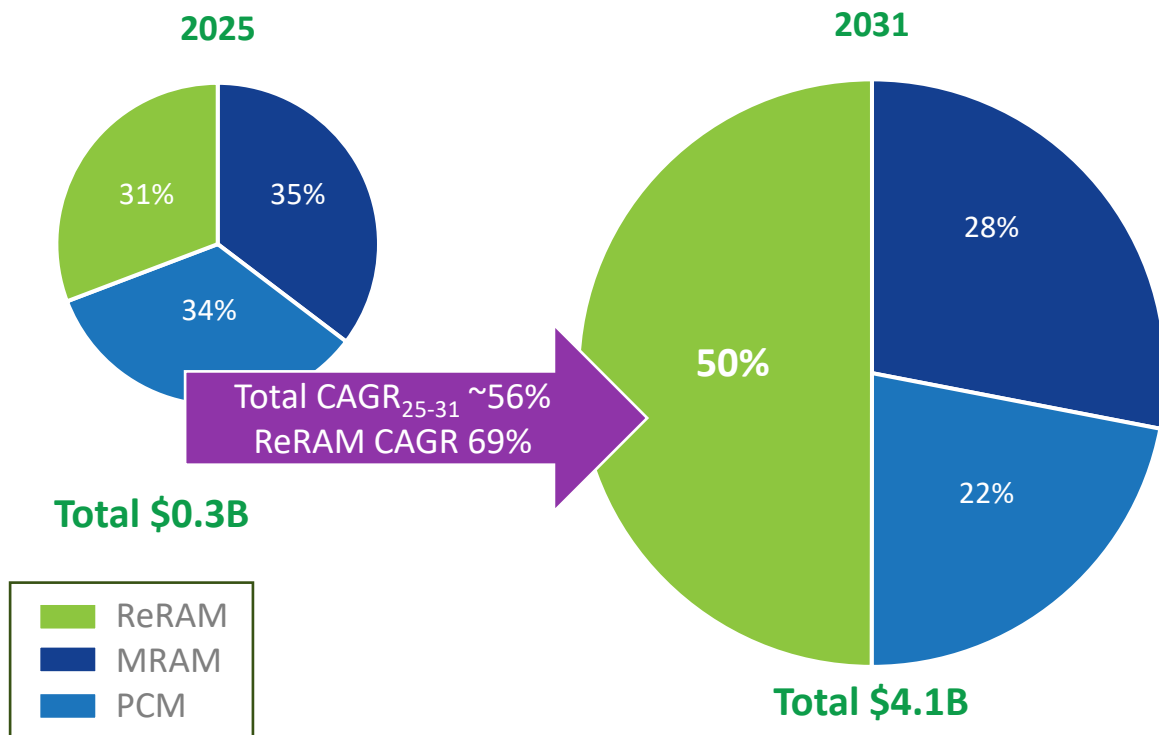
A Brief History of Embedded Non-Volatile Memory (NVM)



The Rise of ReRAM

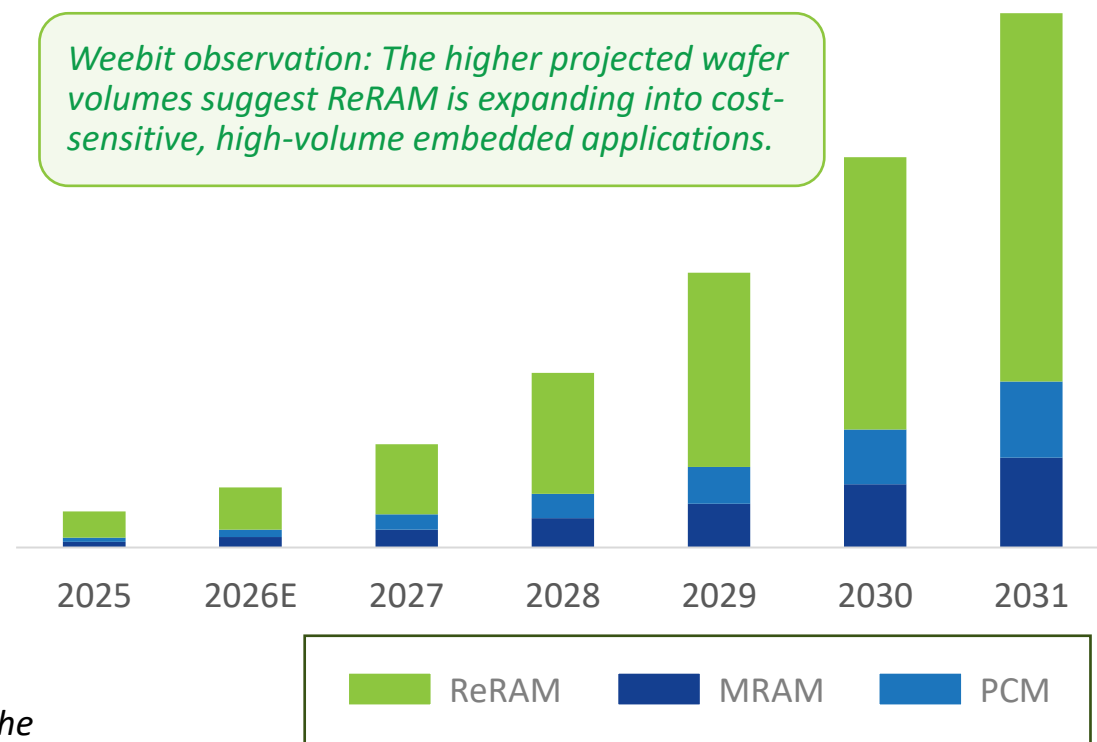
ReRAM outpaces market growth, expanding to 50% share by 2031

Embedded Emerging NVM Market (in \$M)*



Note: The embedded emerging NVM market size is evaluated based on assumptions of the average chip area occupied by a given memory technology (Yole)

Market Volume in # of Eq. 12" Wafers*



Weebit ReRAM Advantages

Superior to embedded flash across key metrics



MANUFACTURING ADVANTAGE

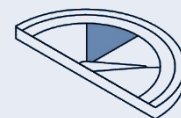
3-4x

Lower added wafer cost
vs. embedded flash

2-mask adder

(vs. ~10)

Shorter cycle time;
fewer manufacturing steps



PERFORMANCE ADVANTAGE

10x-100x

Faster program time
than embedded flash

Bit/byte addressable
No sector erase

>10x

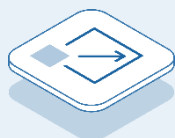
Better endurance
vs. embedded flash

Demo 100K-1M
write cycles

~100x

More energy efficient
vs. embedded flash

Low voltage, low current
write operations



SCALABILITY & INTEGRATION ADVANTAGE

<28nm

Scales to processes far
below limits of flash

Proven @ 28nm and 22nm
Scalable **below**

BEOL¹

0 interference w/ analog
& power devices

Best NVM for PMIC
& mixed-signal



RELIABILITY ADVANTAGE

150°C

Reliable for
Automotive designs

Grade-0 conditions
and profiles

~350x

Better radiation tolerance
vs. flash²

Also tolerant to EMI

Lower cost. Higher performance. Better reliability. Future-proof scalability.

¹ BEOL = Back-End-Of-Line

² Refers to ReRAM cell array



Recent Achievements



Recent Highlights



DEC 2025

Achieved technology qualification at **DB HiTek**



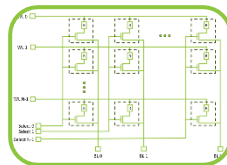
DEC 2025

TI licensed Weebit ReRAM for embedded processing in **advanced nodes**



FEB 2026

onsemi test chips **fully functional**; achieving expected performance



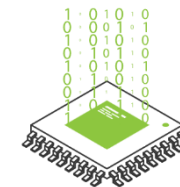
MAR 2026

Weebit ReRAM selected for Korean National **In-Memory Compute (IMC)** Program



MAY 2026

Placement + SPP raised a total of **\$102m**



JUNE 2026

3 product customers tape-out for production to-date; one already demonstrating a functional prototype



JULY 2026

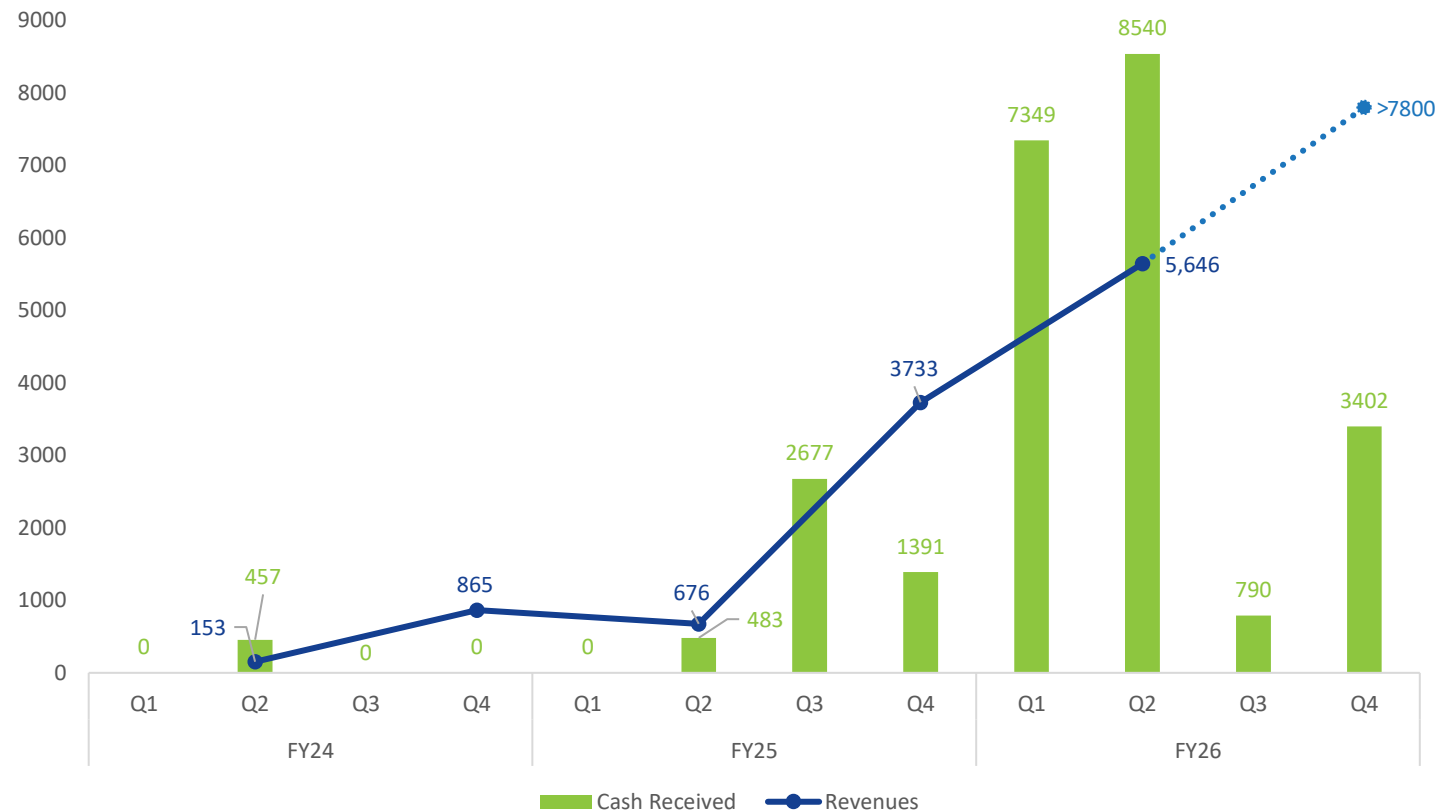
Expanded licensing agreements with key customers to include additional technologies and products

Significant Growth in Revenue and Cash Receipts

- ❖ Quarterly customer receipts of A\$3.4 million in Q2 FY26
 - ◆ Comprising licensing and NRE (non-recurring engineering) fees
 - ◆ Customer receipts are dependent on the timing of license fees and milestone payments
- ❖ Well-funded to execute on growth strategy with a strong cash balance of A\$167 million
- ❖ FY26 guidance upgraded to A\$13.5 million
- ❖ Weebit expects revenues to grow over FY27
 - ◆ New licensing and design agreements
 - ◆ Expansion of existing agreements

Note: Past revenues and cash income do not necessarily reflect on the future

Weebit Past Cash Inflows and Revenues (Thousands A\$)



| Three Product Customer Tape-outs

TAPE-OUT:

The milestone where a completed chip design is released for semiconductor manufacturing

Three product customers have successfully taped out chips incorporating Weebit ReRAM to-date

First silicon received from fab; initial testing confirms chip is fully functional and able to execute software

PRODUCTION
SILICON PROVEN

Characterization and qualification are expected to take approximately 12-18 months before commercial production

Applications include smart battery management systems (BMS), post-quantum security (PQS), and microcontrollers (MCUs)

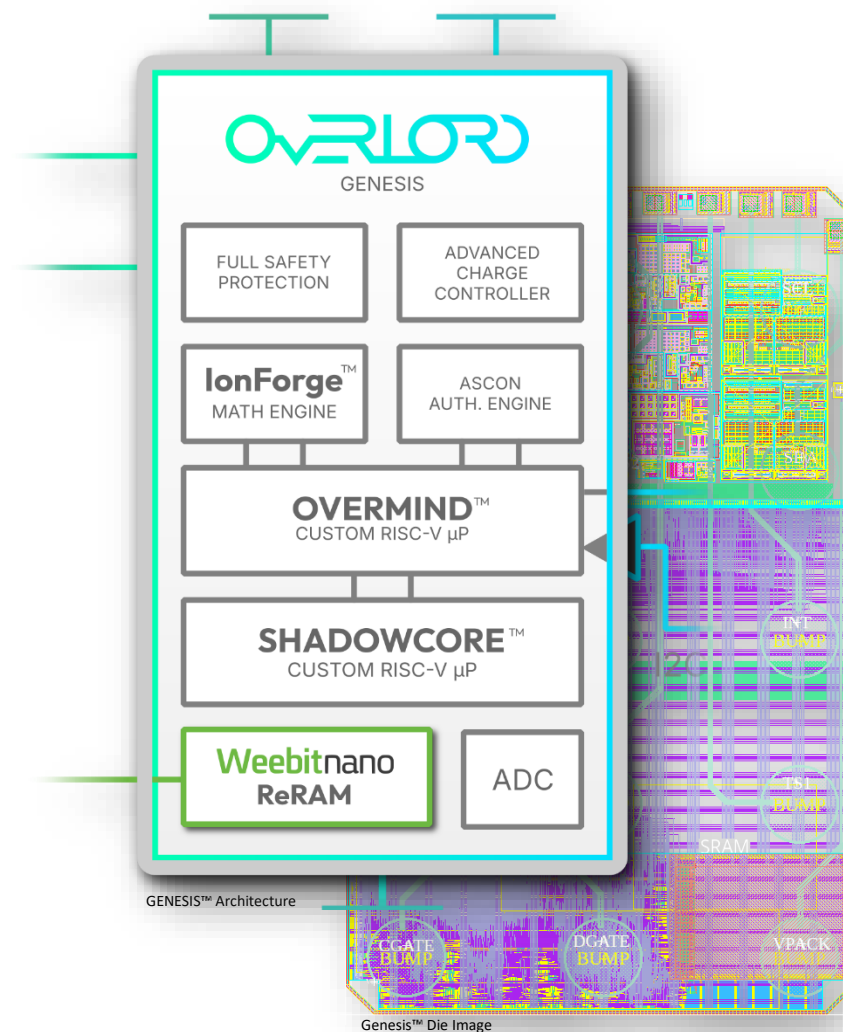
Weebit ReRAM Powering Edge-AI Battery Intelligence

Overlord Labs integrates Weebit ReRAM into its **Battery Intelligence Platform**

- ❖ **GENESIS™** | Silicon × Algorithm Architecture for intelligent battery management
 - ◆ Purpose-built IC for battery-powered **Edge AI devices**
 - ◆ Continuous **battery sensing, modeling & optimization**
 - ◆ Optimized for battery **performance, safety & cycle life**
- ❖ Foundry: DB HiTek
- ❖ First-Silicon: **July 2026**

Why Weebit ReRAM for Genesis™

- Low voltage, ultra-low power embedded NVM
- High-density non-volatile memory
- Bit-addressable architecture
- Higher efficiency and reliability



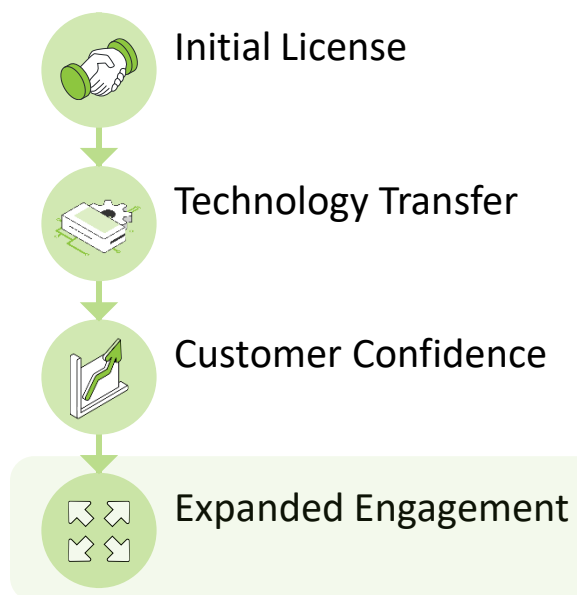
Existing Customers Are Expanding Their Engagement

Growing trust. Broader adoption. Greater long-term value.

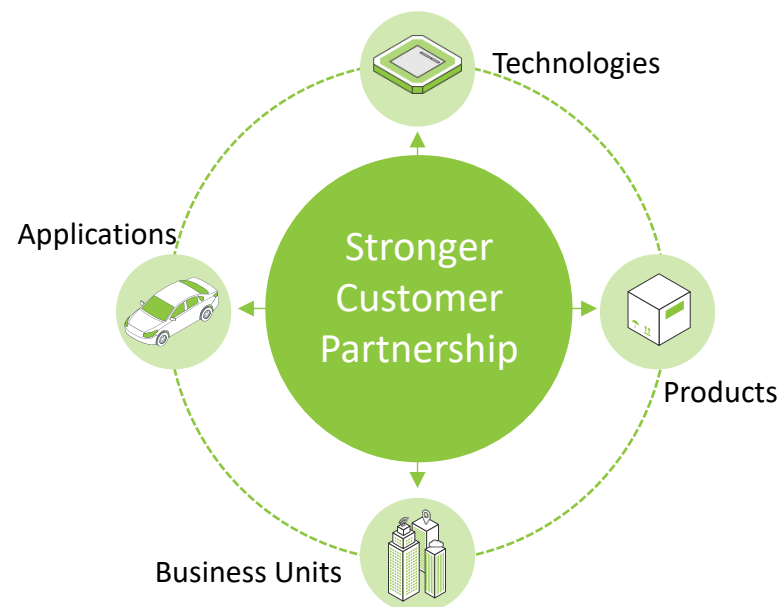
“ Weebit Nano has **expanded its licensing agreements** with key customers to include additional technologies, products and domains. ”

—Weebit 4Q26 report

The Journey to Expanded Engagement



Potential Expansion



The Takeaway



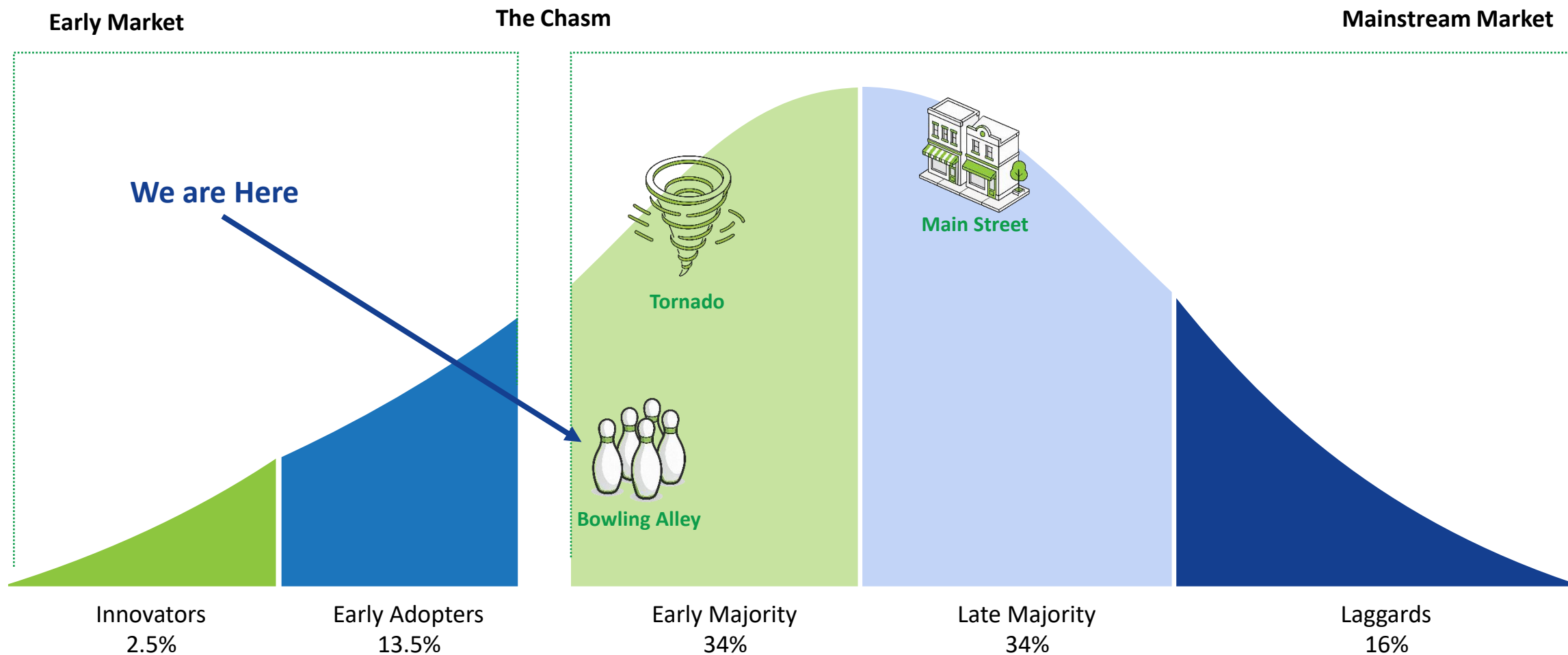
Expanding customer engagement is a powerful indicator of the value of Weebit ReRAM



Opportunities Ahead



Heading for the Tornado

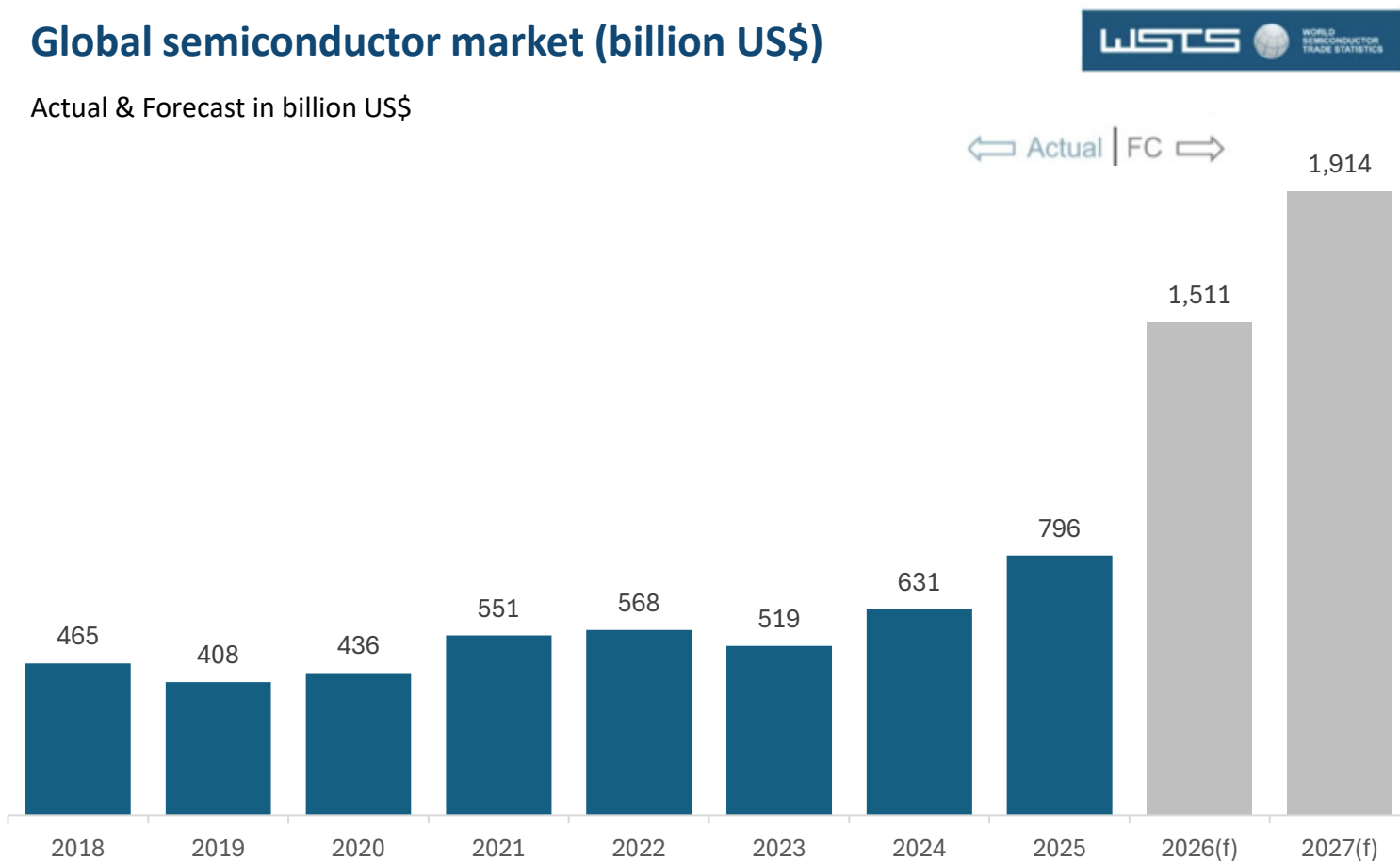


Foundation Diagram courtesy of <https://www.ignitionframework.com/crossing-the-chasm-theory-how-to-market-sell-and-improve-your-new-invention/>

Semiconductor Industry to Hit \$1 Trillion Four Years Faster Than Expected

Global semiconductor market (billion US\$)

Actual & Forecast in billion US\$



Source: https://www.wsts.org/esraCMS/extension/media/f/WST/7618/WSTS_FC-Release-2026-May.pdf

AI drives semiconductor revenues past \$1 trillion for the first time in 2026

With this unprecedented growth:

- ❖ Foundries prioritize revenue-generating customer projects
- ❖ New technologies require expensive fab resources (eng., wafers, etc.)
- ❖ Very limited resources available for new projects / technologies
- ❖ Our customers' attention to new technologies is limited **temporarily**

Strengthening Infrastructure for Rapid Growth

Building the foundation to respond quickly as customer demand accelerates



Broadening the ReRAM Leadership Gap

Continuous R&D to maintain a significant, growing gap over ReRAM competition

R&D Priority	Current Market	Weebit Target	Customer Benefit
Higher performance	>20ns access time	<10ns access time	Expand applications in MCU market
Higher endurance	10K cycles	1M cycles	Supports demanding automotive and storage workloads
Automotive capability	10 yrs @ 105° - 125°C	Lifetime @ 150°C 10 yrs @ 175°C	Expands automotive, power and industrial opportunities
Automotive architecture	2T2R	1T1R (~2x smaller)	Lower system cost and power consumption

GOALS:

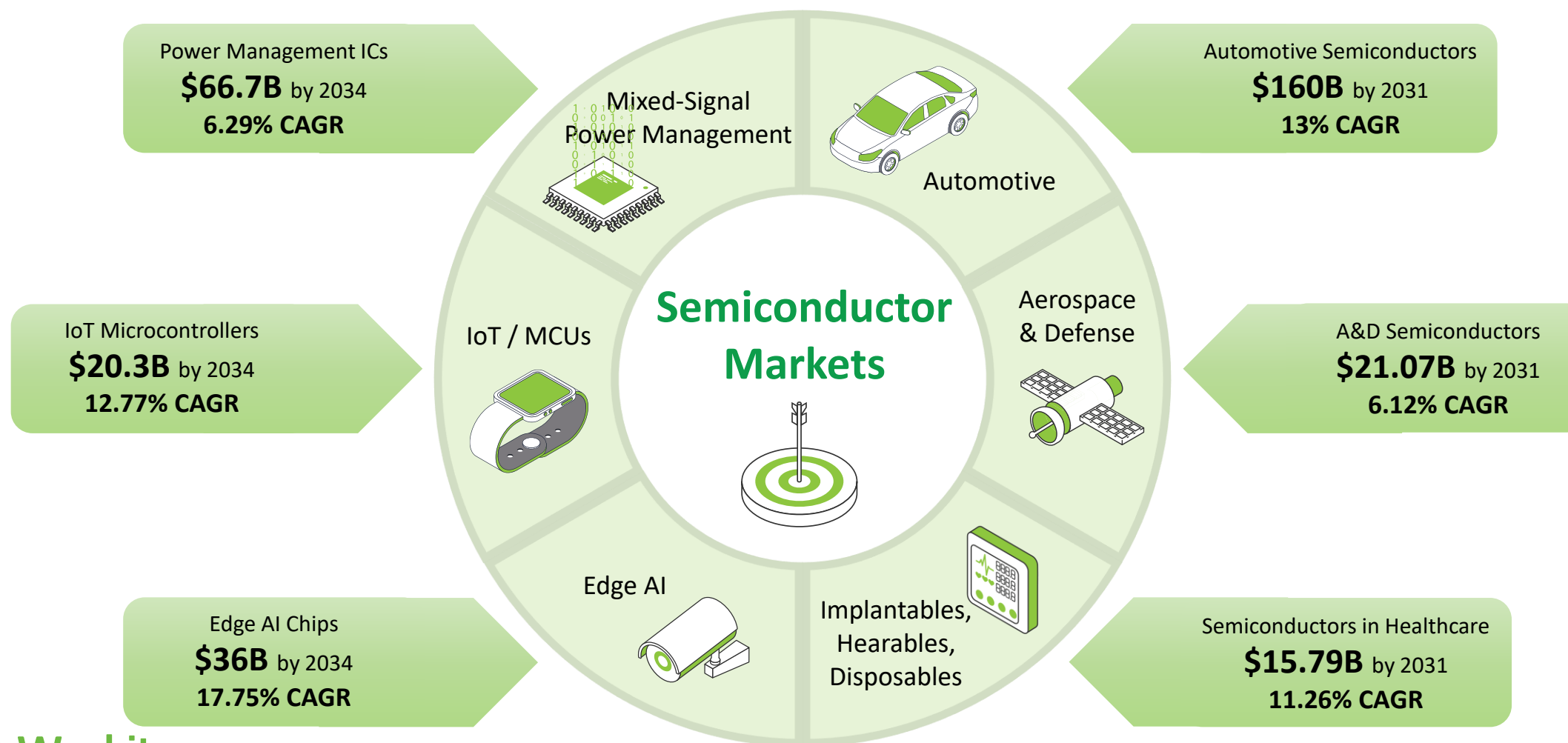
- ❖ Extend **technology leadership**
- ❖ Accelerate **customer adoption**
- ❖ Drive long-term **royalties**



Supported by
proceeds from the
recent capital raise

Addressing Fast-Growing Semiconductor Markets

>US\$320 Billion TAM in Customer Markets



Weebit ReRAM: A Foundation for Memory + Compute

One technology. Multiple architectures. Broad opportunity.

TODAY: Embedded ReRAM

- ❖ Commercially available
- ❖ Qualified on multiple platforms
- ❖ Growing customer adoption
- ❖ Generating revenues

EMERGING: Memory Centric Computing

- ❖ Convergence of memory and compute is transforming AI architectures
- ❖ A growing opportunity for ReRAM

NEAR-MEMORY
COMPUTE



IN-MEMORY
COMPUTE



NEUROMORPHIC
COMPUTE



Why ReRAM?

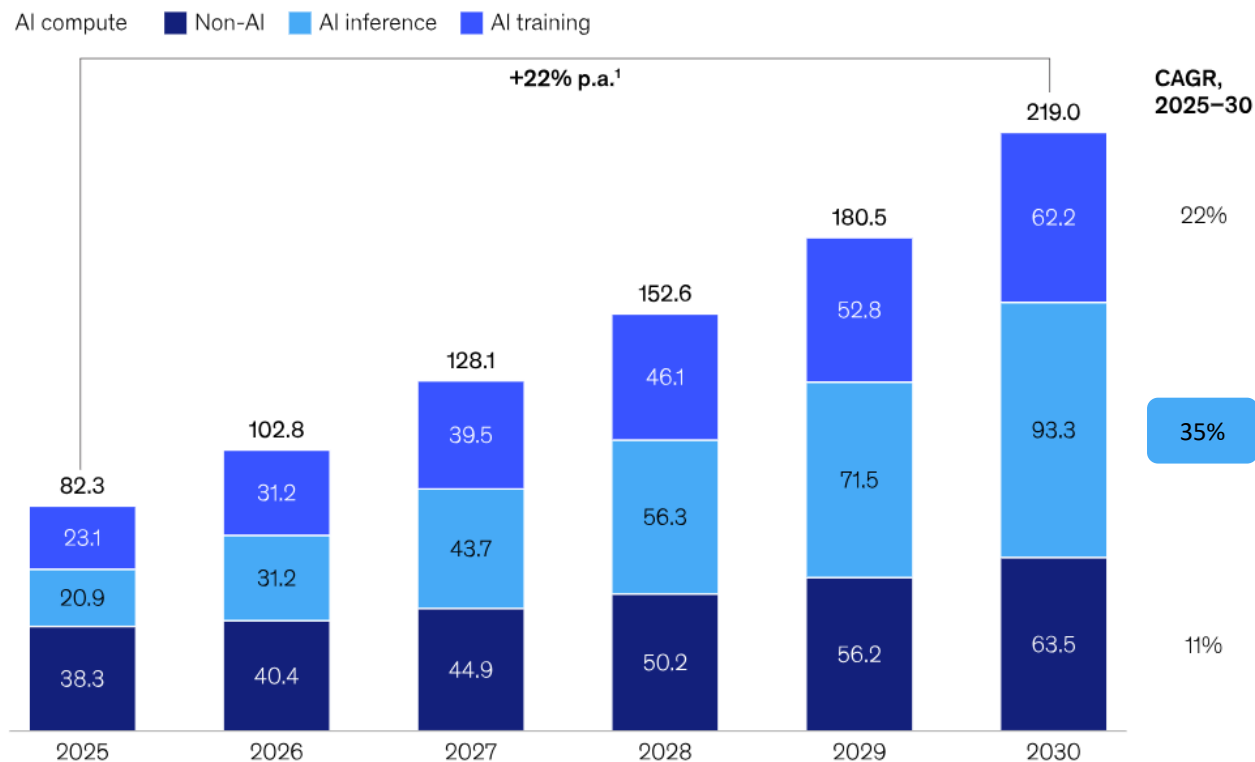
	Storage	Analog Compute (IMC)	Scalability
Flash	+	+	--
MRAM	+	--	+
ReRAM	+	+	+

💡 The future of AI computing requires memory that is manufacturable, scalable, low-cost, and IMC-ready. Weebit ReRAM is one of the few emerging NVM technologies approaching commercial production.

The Next Phase of AI is About Inference

Inference workloads could make up more than 40% of data center demand in 2030

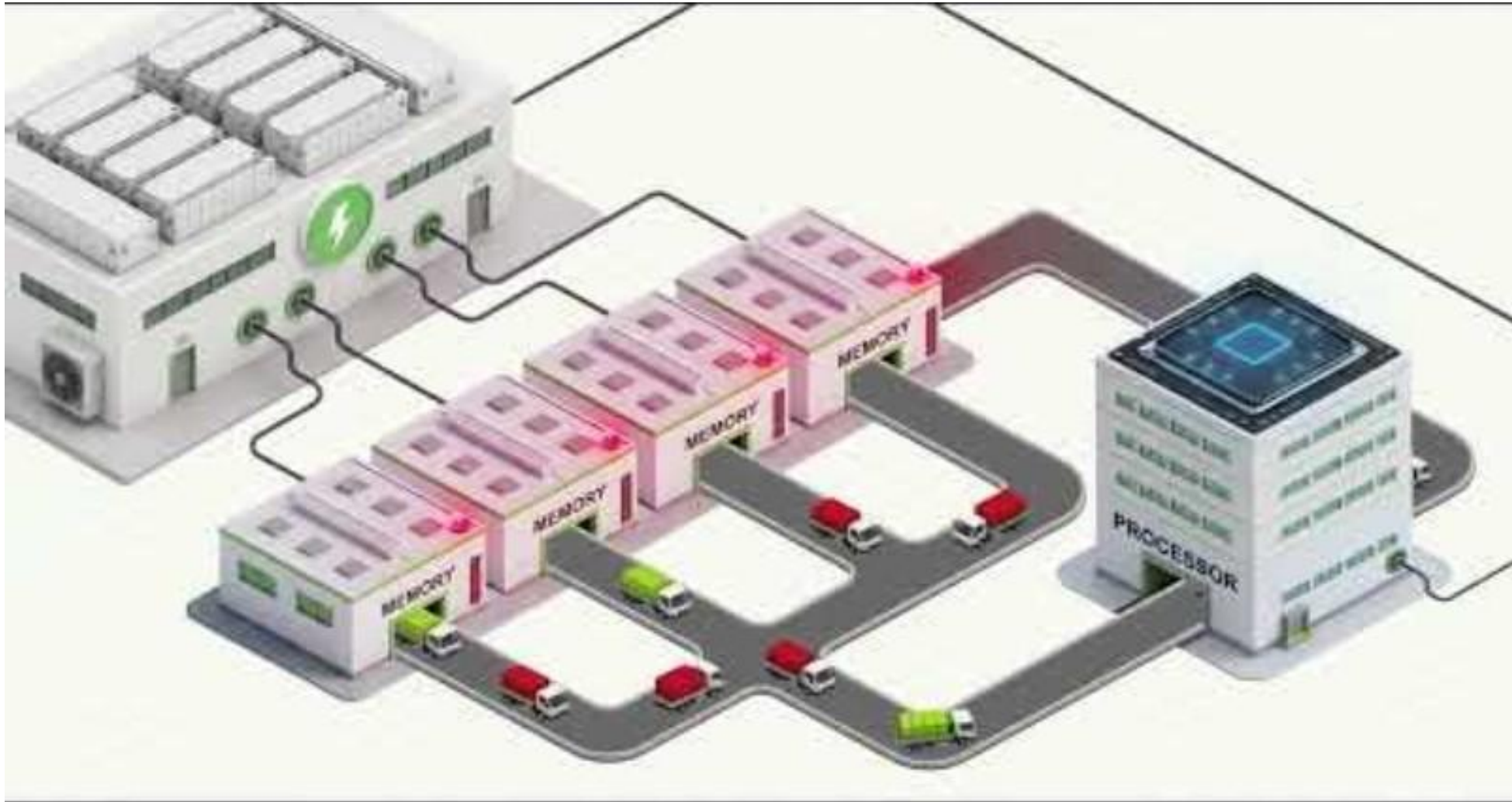
Global data center demand by workload, 2025-30, gigawatts



AI is moving from a training-centric to an inference-driven deployment model

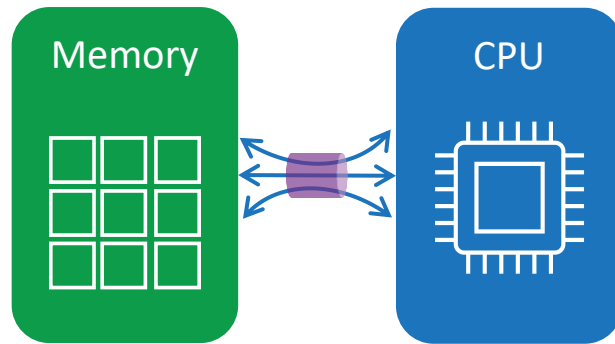
- ◆ Hyperscale datacenters
 - ◆ Industrial systems
 - ◆ Vehicles
 - ◆ Edge devices
 - ◆ Physical AI systems
- ◆ Energy efficiency and response time are critical design constraints
- ◆ Pressure on memory architectures is creating new opportunities for advanced NVM technologies

| In-Memory Computing: A New Way to Process Data



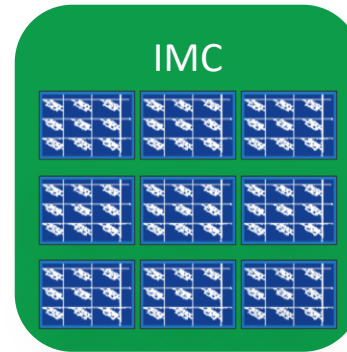
ReRAM: Scalable NVM for Future In-Memory AI Computing

Traditional Architectures: AI Compute Bottleneck



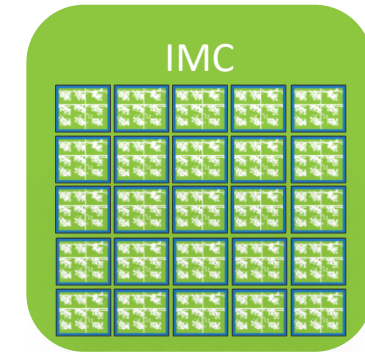
- ❖ **Data movement consumes most power**
- ❖ Limits performance
- ❖ Moving to Near Memory Compute where memory and compute are on same chip

Initial IMC Implementations Show Feasibility; Not Scalability



- ❖ Analog matrix multiplication in flash memory crossbars
- ❖ Enables major energy efficiency gains
- ❖ Limitations:
 - **Does not scale** to advanced nodes
 - **High programming voltage**
 - **Slow** updates

ReRAM Enables IMC Better Aligned with AI Workloads



- ❖ **Highly scalable** crossbar arrays
- ❖ **Fast** write speeds
- ❖ **Low voltage** operation
- ❖ **Multi-level** analog weights
- ❖ Potential to support **inference in both data centers and edge devices**

| Recent Customer Evaluation: Weebit ReRAM for IMC

A U.S. based developer of radiation-tolerant AI hardware evaluated a next-generation AI accelerator using Weebit ReRAM for IMC research.*



>80x

**Lower power
consumption
vs GPU**



~280x

**Higher compute
efficiency
than FPGA**



~100x

**Lower inference
latency
than GPU**

[Read the article](#)

*GPU: NVIDIA GeForce RTX 4090; FPGA: Xilinx ZCU104 (Zynq UltraScale+); IMC system-level performance projected from measured Weebit ReRAM IMC hardware results using application-level modeling.

Direct link to the article: <https://www.weebit-nano.com/researchers-explore-next-generation-ai-architectures-using-weebit-reram>

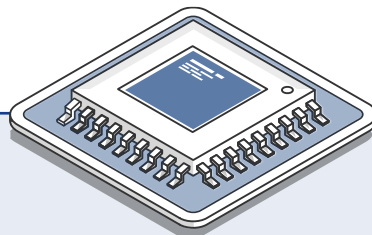
Weebit Nano Key Targets for CY 2026



Revenue

FY26 revenue goal
>\$10M

EXPECTED TO BE
ACHIEVED



Products

First product
company customer
tape-out

ACHIEVED.
3 TAPE-OUTS COMPLETED



Customers

First AI customer
engagement

ON TRACK

**Delivering on these milestones will continue to cement Weebit's position
as the leading independent provider of ReRAM technology**

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